400W isolated DC-DC converter
Wide input and regulated single output



6

EN50155 Patent Protection RoHS



- Wide input voltage range: 66 160 VDC
- High efficiency up to 92%
- Reinforced insulation, I/O isolation test voltage 3k VAC, Input-case test voltage 2.1k VAC
- Operating ambient temperature range -40℃ to +100℃
- Input under-voltage protection, output over-current, over-voltage, short-circuit protection, over-temperature protection
- Industry standard full-brick and pin-out
- Meet IEC/EN/UL62368/EN50155 standards

URF1D_FB-400(H)WR3 series is a high-performance product specifically designed for a variety of railway applications. The DC-DC converters feature 400W output power with no requirement for minimum load, wide input voltage from 66-160VDC, and allowing operating temperature as high as 100°C. Additional product features include input under-voltage protection, output over-current, over-voltage, short-circuit and over-temperature protection, remote On/Off control, remote sense compensation, output voltage trim adjustment. The products meet IEC/EN/UL62368/EN50155 standards and they are widely used in railway systems and associated equipment.

Selection	Guide							
		Ctrl	Input Volta	Input Voltage (VDC)		Output		Capacitive
Certification	Part No. [®]	Logic [®]	Nominal (Range)	Max.®	Voltage (VDC)	Current (mA) Max./Min.	Efficiency (%) Min./Typ.	Load (µF)Max.
	URF1D05FB-400W(H)R3				5	64000/0	84/86	10000
	URF1D09FB-400W(H)R3			170	9	44440/0	88/90	6800
	URF1D12FB-400W(H)R3				12	33330/0	89/91	4000
	URF1D15FB-400W(H)R3				15	26670/0	89/91	4000
EN	URF1D24FB-400W(H)R3	P	110 (66-160)		24	16670/0	90/92	2700
	URF1D28FB-400W(H)R3	F	(00-100)		28	14290/0	90/92	2700
	URF1D36FB-400W(H)R3				36	11111/0	90/92	680
	URF1D48FB-400W(H)R3				48	8333/0	90/92	680
	URF1D54FB-400W(H)R3				54	7410/0	90/92	680

Note:

①Use "H" suffix for heat sink mounting. We recommend to choose modules with a heat sink for enhanced heat dissipation and applications with extreme temperature requirements;

[®]Exceeding the maximum input voltage may cause permanent damage.

Item	Operating Conditions		Min.	Тур.	Max.	Unit
		5V output	_	4229/110	4329/140	
	Name in all in most yealth area	9V output	_	4041/110	4132/140	
Input Current (full load/no-load)	Nominal input voltage	12V, 15V output	_	3996/110	4086/140	mA
		Others	_	3953/110	4041/140	
Reflected Ripple Current	Nominal input voltage	Nominal input voltage				
Surge Voltage (1sec. max.)			-0.7		185	
Start-up Voltage		-		66	VDC	
Input Under-voltage Protection			55	58		
Start-up Time	Nominal input voltage, consta	nt resistance load		40	100	ms
Input Filter				Pi filt	er	
Hot Plug				Unavai	able	
Ctrl [⊕]	Module on	Ctrl open circuit or connected to TL high leve (3.5-12VDC)				
	Module off		Ctrl pin	connected t (0-1.2V		level
	Input current when off			5	10	mA

MORNSUN®

^{2&}quot;P" means positive logic, "N" means negative logic;

DC/DC Converter URF1D_FB-400W(H)R3 Series



Idle input power	Ctrl pin pulled low to -Vin, DC-DC OFF (66-160V input)	OC-DC OFF (66-160V input)			
Note: ①The Ctrl pin voltage is referenc	ed to input -Vin.				

Output Specifications						
Item	Operating Conditions		Min.	Тур.	Max.	Unit
Voltage Accuracy	Nominal input voltage, 0%-100% load			±1	±3	
Linear Regulation	Input voltage variation from low to high	at full load		±0.2	±0.5	ov.
Land Danidaktan	Name of the state of 1000/ land	5V output		±0.8	±1.0	%
Load Regulation	Nominal input voltage, 0%-100% load	Others		±0.4	±0.5	
Transient Recovery Time	050/ la salatara altara alta an 205° 0	25% load step change @25°C			500	μs
Transient Response Deviation	25% load step change @25 C				±5	%
Temperature Coefficient	Full load	Full load			±0.03	%/℃
Ripple & Noise [®]	20MHz bandwidth, 10%-100% load			150	200	mVp-p
Trim			90	_	110	0() (-
Sense				-	105	%Vo
Over-temperature Protection	Max. Case Temperature			105	115	°C
Over-voltage Protection			110	130	160	%Vo
Over-current Protection	Input voltage range	110	140	150	%lo	
Short-circuit Protection			Hiccu	up, continuo	us, self-reco	very
Note: ①For ripple and noise measurin	ng method, please refer to Fig. 1.					

General Specification	ons							
Item	Operating Conditions	Operating Conditions			Max.	Unit		
	5, 1, 0, 1, 7, 1, 7, 1, 1, 11, 11, 11, 11, 11,	Input-output	3000					
Isolation	Electric Strength Test for 1 minute with a leakage current of 5mA max	Input-case	2100			VAC		
	leakage caller of on with ax	Output-case	1500					
Insulation Resistance	Input-output resistance at 500VDC		100	-	-	$\mathbf{M}\Omega$		
Isolation Capacitance	Input-output capacitance at 100kHz/0.1V			1000	_	рF		
Operating Temperature	See temperature derating curves		-40	-	+100	°C		
Storage Temperature					+125			
Storage Humidity	Non-condensing	Non-condensing			95	%RH		
Pin Soldering Resistance	Wave-soldering, 10 seconds		-	260	°C			
Temperature	Soldering spot is 1.5mm away from case for	Soldering spot is 1.5mm away from case for 10 seconds		om case for 10 seconds			00 +100 - +125 - 95 - 260 - 300 N60068-2-1	
Cooling Requirement				EN6006	8-2-1			
Dry-heat Requirement			EN60068-2-2					
Damp-heat Requirement			EN60068-2-30					
Shock And Vibration					gory 1, Gro	ide B		
Switching Frequency	PFM mode	PFM mode				kHz		
MTBF	MIL-HDBK-217F@25°C		250	-	-	k hours		

Mechanical Specifications							
Case Material	Aluminum alloy bottom, flame-retardar	uminum alloy bottom, flame-retardant and heat-resistant (PA66)					
Discounting	URF1D_FB-400WR3	116.80 x 61.00 x 13.00mm					
Dimension	URF1D_FB-400WHR3	116.80 x 61.00 x 31.00mm					
\A/-!	URF1D_FB-400WR3	272g (Typ.)					
Weight	URF1D_FB-400WHR3	428g (Typ.)					
Cooling Method	Free air convection (20LFM) or forced of	air convection					

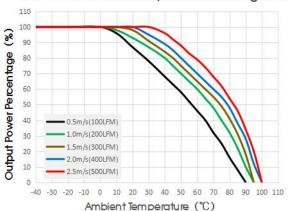


Electrom	Electromagnetic Compatibility (EMC)									
Emissions	CE	CISPR32/EN55032	CLASS A (see Fig. 3 for recommended circuit)						
ETTISSIONS	RE	CISPR32/EN55032	CLASS A (see Fig. 3 for recommended circuit)						
	ESD	IEC/EN61000-4-2	GB/T17626.2	Contact ±6kV/Air ±8kV	perf.Criteria A					
	RS	IEC/EN61000-4-3	GB/T17626.3	20V/m	perf.Criteria A					
Immunity	EFT	IEC/EN61000-4-4	GB/T17626.4	±2kV(5kHz, 100kHz) (see Fig. 3 for recommended circuit)	perf.Criteria A					
in in its in its	Surge	IEC/EN61000-4-5	GB/T17626.5	line to line ±2kV (1.2 μ s/50 μ s, 2 Ω) (see Fig. 3 for recommended circuit)	perf.Criteria A					
	CS	IEC/EN61000-4-6	GB/T17626.6	10 Vr.m.s	perf.Criteria A					

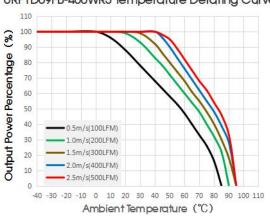
Electrom	Electromagnetic Compatibility (EMC) (EN50155)								
	CE	EN50121-3-2 150kHz-500kHz 99dBuV (see Fig. 3 for recommended circuit)							
Emissions	OL .	EN55016-2-1 500kHz-30MHz 93dBuV							
ELLISSIOLIS	RE	EN50121-3-2 30MHz-230MHz 40dBuV/m at 10m (see Fig. 3 for recommended circuit)							
	INL.	EN55016-2-1 230MHz-1GHz 47dBuV/m at 10m							
	ESD	EN50121-3-2 Contact ±6kV/Air ±8kV	perf. Criteria A						
	RS	EN50121-3-2 20V/m	perf. Criteria A						
Immunity	EFT	EN50121-3-2 ±2kV 5/50ns 5kHz (see Fig. 3 for recommended circuit)	perf. Criteria A						
	Surge	EN50121-3-2 line to line ± 1 kV (42 Ω , 0.5 μ F) (see Fig .3 for recommended circuit)	perf. Criteria A						
	CS	EN50121-3-2 0.15MHz-80MHz 10 Vr.m.s	perf. Criteria A						

Typical Performance Curves

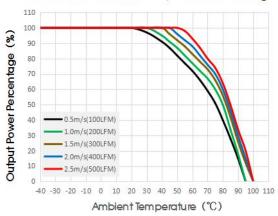




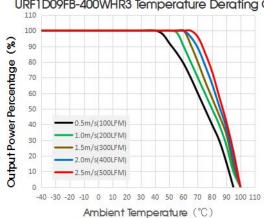
URF1D09FB-400WR3 Temperature Derating Curves

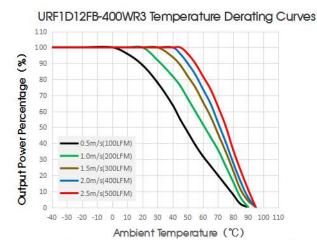


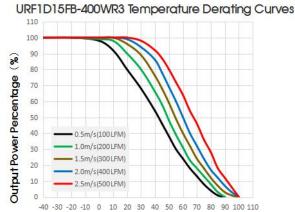
URF1D05FB-400WHR3 Temperature Derating Curves



URF1D09FB-400WHR3 Temperature Derating Curves

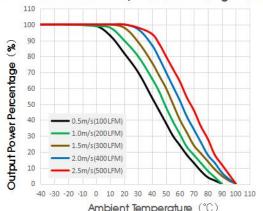




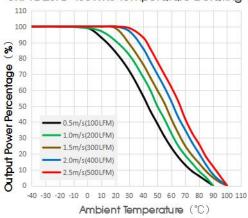


URF1D24FB-400WR3 Temperature Derating Curves

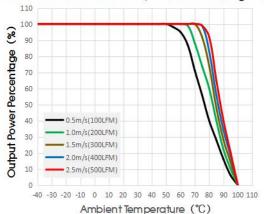
Ambient Temperature (°C)



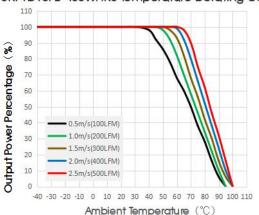
URF1D28FB-400WR3 Temperature Derating Curves



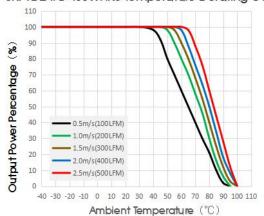
URF1D12FB-400WHR3 Temperature Derating Curves



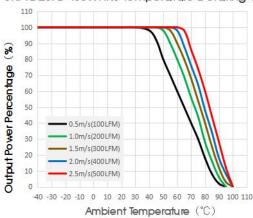
URF1D15FB-400WHR3 Temperature Derating Curves



URF1D24FB-400WHR3 Temperature Derating Curves

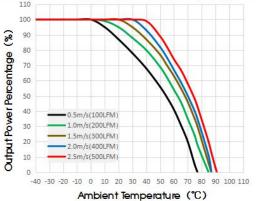


URF1D28FB-400WHR3 Temperature Derating Curves

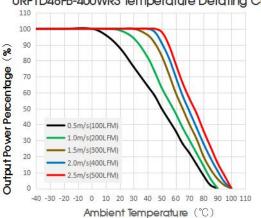


MORNSUN®

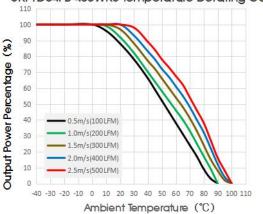




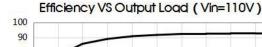
URF1D48FB-400WR3 Temperature Derating Curves

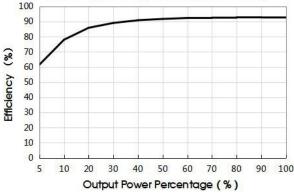


URF1D54FB-400WR3 Temperature Derating Curves

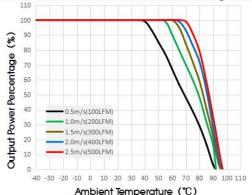


URF1D24FB-400WR3

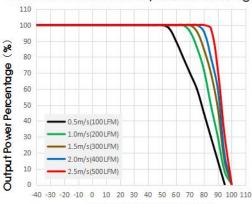




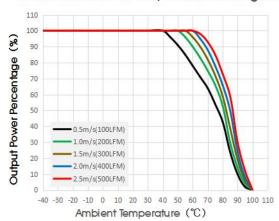
URF1D36FB-400WHR3 Temperature Derating Curves



URF1D48FB-400WHR3 Temperature Derating Curves

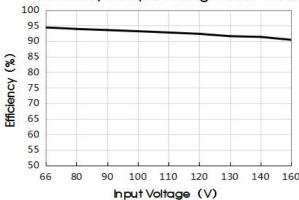


Ambient Temperature (°C) URF1D54FB-400WHR3 Temperature Derating Curves



URF1D24FB-400WR3

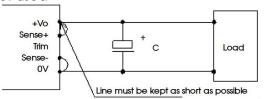
Efficiency VS Input Voltage (Full Load)



MORNSUN®

Remote Sense Application

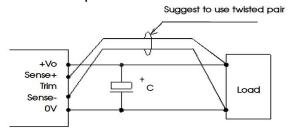
1. Remote Sense Connection if not used



Notes:

- (1) If the sense function is not used for remote regulation the user must connect the +Sense to +Vo and -Sense to 0V at the DC-DC converter pins and will compensate for voltage drop across pins only.
- (2) The connections between Sense lines and their respective power lines must be kept as short as possible, otherwise they may be picking up noise, interference and/or causing unstable operation of the power module.

2. Remote Sense Connection used for Compensation



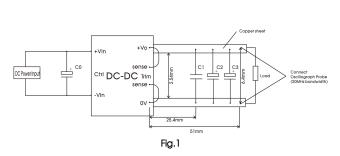
Notes:

- (1) Using remote sense with long wires may cause unstable output, please contact technical support if long wires must be used.
- (2) PCB-tracks or cables/wires for Remote Sense must be kept as short as possible. Twisted pair or shielded wires are suggested for remote compensation and must be kept as short as possible.
- (3) We recommend using adequate cross section for PCB-track layout and/or cables to connect the power supply module to the load in order to keep the voltage drop below 0.3V and to make sure the power supply's output voltage remains within the specified range.
- (4) Note that large wire impedance may cause oscillation of the output voltage and/or increased ripple. Consult technical support or factory for further advice of sense operation.

Design Reference

1. Ripple & noise

All the DC-DC converters of this series are tested before delivery using the recommended circuit shown in Fig. 1.

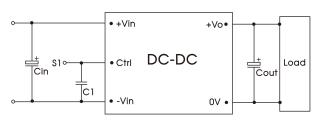


Capacitors Output value voltage	C0	C1	C2	C3	
5VDC					
9VDC		105k/50V	10µF/35V	680µF/35V	
12VDC	100µF	ceramic capacitor	tantalum capacitor	electrolytic	
15VDC	aluminum			capacitor	
24VDC	Electrolytic capacitor				
28VDC	(Voltage≥				
36VDC	200V)	105k/100V ceramic		220µF/100V electrolytic	
48VDC		capacitor		capacitor	
54VDC					

2. Typical application

We recommended using Mornsun's EMC circuit, otherwise please ensure that at least a 100µF electrolytic capacitors is connected at the input in order to ensure adequate voltage surge suppression and protection.

Input and/or output ripple can be further reduced by appropriately increasing the input & output capacitor values Cin and Cout and/or by selecting capacitors with a low ESR (equivalent series resistance). Also make sure that the capacitance is not exceeding the specified max. capacitive load value of the product.



Capacitor Valu Output Voltage	Cout	Cin	C1
5V/9V/12V/15V/24V/ 28V/36V/48V/54V	220µF/63V	100 µF/200V	104k/50V

Fig.2

EMC solution-recommended circuit

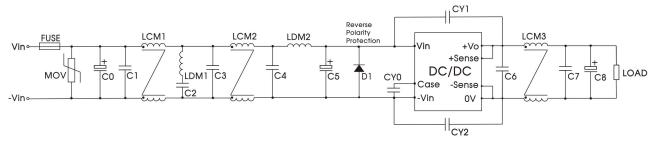
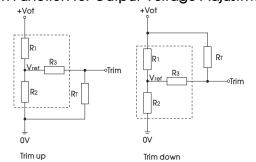


Fig. 3

Common on to	Recomm	nended Component val	ue				
Components	05V, 09V, 12V, 15V	24V, 36V, 48V, 54V	28V				
CY1	2.2nF/400VAC Y1 safety capacitor	2.2nF/400VAC Y1 safety capacitor	3.2nF/400VAC Y1 safety capacitor				
CY2	2.2nF/400VAC Y1 safety capacitor	4.4nF/400VAC Y1 safety capacitor	4.4nF/400VAC Y1 safety capacitor				
FUSE	Choose acc	cording to actual input o	current				
MOV		S20K130 (Varistor)					
D1		250V/20A diode					
C0	330µF/250V electrolytic capacitor						
C5	330µF/2	00V electrolytic capacit	or				
C8	220µF/6	63V electrolytic capacito	or				
C1, C2, C3, C4, C6, C7	2.2µF/	250V ceramic capacito	r				
LCM1	Mor	nsun P/N: FL2D-60-451					
LCM2	Mor	nsun P/N: FL2D-60-402					
LCM3	5V/9V output	Mornsun P/N:	FL2D-F5-040				
LCIVIS	Others	Mornsun P/N:	FL2D-D0-040				
LDM1	0.47	7uH Shielded inductor					
LDM2	1.5	uH Shielded inductor					
CY0	2.2nF/40	00VAC Y1 safety capacit	or				

4. Trim Function for Output Voltage Adjustment (open if unused)



TRIM resistor connection (dashed line shows internal resistor network) Fig.4

Calculation formula of Trim resistance:

up:
$$RT = \frac{GR2}{R2-G} - R3$$
 $a = \frac{Vref}{Vo' - Vref} \cdot R1$
down: $RT = \frac{GR1}{R1-G} - R3$ $a = \frac{Vo' - Vref}{Vo' - Vref} \cdot R2$

Note:

Value for R1, R2, R3, and V_{ref} refer to the above table 1.

 $R_{\text{\tiny{T}}}\!\!:$ Resistance of Trim.

a: User-defined parameter, no actual meanings.

Vo': The trim up/down voltage.

Vo Res	5(VDC)	9(VDC)	12(VDC)	15(VDC)	24(VDC)	28(VDC)	36(VDC)	48(VDC)	54(VDC)
R1(kΩ)	2.92	7.58	11	14.49	24.87	29.4	68	58.69	60.77
R2(kΩ)	2.87	2.87	2.87	2.87	2.87	2.87	5.04	3.21	2.94
R3(kΩ)	8.66	15	17.8	20	20	20	27	20	20
Vref(V)	2.5	2.5	2.5	2.5	2.5	2.5	2.5	2.5	2.5

Practical Example trim up +10% for 12V output:

Practical Example trim up -10% for 12V output:

$$\alpha = \frac{2.5*11}{13.2 \cdot 2.5} = 2.57 \quad R_{\scriptscriptstyle T} = \frac{2.57*2.87}{2.87 \cdot 2.57} \cdot 17.8 = 6.786 \text{K} \,\Omega$$

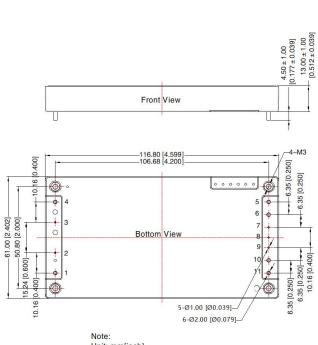
$$b = \frac{(10.8 - 2.5) * 2.87}{2.5} = 9.53 \quad R_{_{T}} = \frac{9.53 * 11}{11 - 9.53} - 17.8 = 53.51 \text{K}\,\Omega$$

 R_T according to E24pprox6.8k Ω

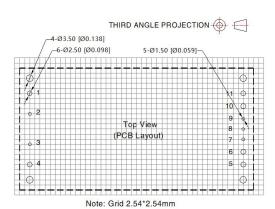
 R_T according to E24pprox53.6 $k\,\Omega$

- The products do not support parallel connection of their output
- For additional information please refer to DC-DC converter application notes on www.mornsun-power.com

URF1D_FB-400WR3 Dimensions and Recommended Layout



Unit: mm[inch] Pin1, 4, 5, 6, 10, 11's diameter: 2.00[0.079] Pin2, 3, 7, 8, 9's diameter: 1.00[0.039] Pin diameter tolerances: ± 0.10[± 0.004] General tolerances: ± 0.50[± 0.020] Mounting hole screwing torque: Max 0.4 N · m

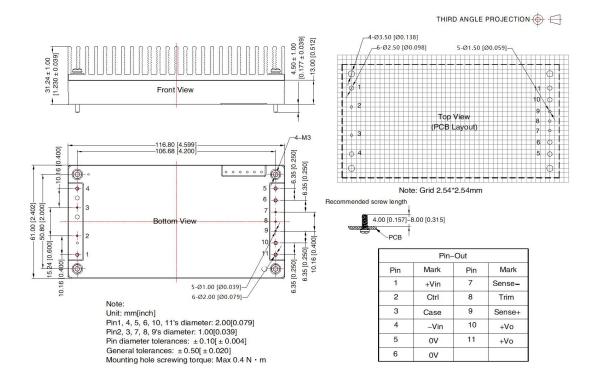




Pin-Out			
Pin	Mark	Pin	Mark
1	+Vin	7	Sense-
2	Ctrl	8	Trim
3	Case	9	Sense+
4	-Vin	10	+Vo
5	OV	11	+Vo
6	OV		



URF1D_FB-400WHR3 Dimensions and Recommended Layout



Note:

- For additional information on Product Packaging please refer to <u>www.mornsun-power.com</u>. Packaging bag number: 58210118 (without heatsink), 58220029(with heatsink);
- It is suggested to use module at load of over 5%, if not, the ripple of the product may exceeds the specification, but does not affect the reliability of the product;
- It is recommended that the load imbalance of the dual output is ≤±5%. If it exceeds ±5%, the performance of the product cannot be guaranteed to meet as datasheet marked. For details, please contact our technical staff;
- The maximum capacitive load offered were tested at input voltage range and full load;
- 5. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated output load;
- 6. All index testing methods in this datasheet are based on company corporate standards;
- 7. We can provide product customization service, please contact our technicians directly for specific information;
- 8. Products are related to laws and regulations: see "Features" and "EMC";
- Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

MORNSUN Guangzhou Science & Technology Co., Ltd.

Address: No. 8 Nanyun 4th Road, Huangpu District, Guangzhou, China

Tel: 86-20-38601850 Fax: 86-20-38601272 E-mail: info@mornsun.cn www.mornsun-power.com

MORNSUN®