

3W Dual isolated DC-DC converter in SIP package Ultra-wide input and regulated dual output



## FEATURES

- Ultra-wide 4:1 input voltage range
- No-load power consumption as low as 0.2W
- I/O isolation test voltage: 3k VDC
- Input under-voltage protection, output

short-circuit, over-current protection

• Operating ambient temperature range: -40 $^{\circ}$ C to

**+85**℃

 $URD_S-3WR3$  series of isolated 3W DC-DC converter products with an ultra-wide 4:1 input voltage, input to output isolation is tested with 3000VDC and the converters safely operate ambient temperature of -40°C to +85°C, input under-voltage protection, output short-circuit, over-current protection. They meets CLASS B of CISPR32/EN55032 EMI standards without extra components, which make them widely applied in data transmission device, tele-comunication device, distributed power supply system, hybrid module system, remote control system fields.

Selection	Guide							
		Input Voltage (VDC)		C	Output(Vo1/Vo2)			Capacitive
Certification	Part No.	Nominal (Range)	Max.®	Voltage(VDC)	Current (mA) Max.	Current (mA) Min.	Efficiency <sup>®</sup> (%) Min./Typ.	Load (µF)Max. (Vo1/Vo2)
	URD480505S-3WR3			5/5	300/300	0/0	76/78	680/680
EN/BS EN	URD480512S-3WR3	48 (18-75)	80	5/12	300/125	0/0	76/78	680/330
	URD480524S-3WR3			5/24	300/63	0/0	76/78	680/220

Notes:

 $(\underline{)}$  Exceeding the maximum input voltage may cause permanent damage;

2 Efficiency is measured In nominal input voltage and rated output load.

# Input Specifications

Item	Operating Conditions	Min.	Тур.	Max.	Unit
Input Current (full load / no-load)	Naminal input voltage		81/5	83/12	mA
Reflected Ripple Current	Nominal input voltage		30		
Surge Voltage (1sec. max.)		-0.7		100	
Start-up Voltage				18	VDC
Shut-down Voltage		12	15		
Start-up Time	Nominal input& constant resistance load		10		ms
Input Filter			Capacito	ince Filter	
Hot Plug		Unavailable			

<b>Output Specifications</b>						
Item	Operating Conditions		Min.	Тур.	Max.	Unit
		Vo1		±l	±3	
Voltage Accuracy	10% -100% load	Vo2		±3	±5	
Voltage Accuracy		Vo1		±2	±4	
	5% -10% load	Vo2		±4	±6	
	Input voltage variation from low to high	Vo1		±0.2	±0.5	%
Linear Regulation <sup><math>\omega</math></sup>	at full load	Vo2		±0.5	±1	
				±0.5	±1	
Load Regulation 10% -100% load		Vo2		±l	±2	
Cross Regulation	Vo1 load at 50%, Vo2 load at range of 25%-100%				±8	

**MORNSUN®** 

#### MORNSUN Guangzhou Science & Technology Co., Ltd.

2021.11.20-A/4 Page 1 of 5

# DC/DC Converter URD\_S-3WR3 Series

# **MORNSUN**<sup>®</sup>

Transient Recovery Time	25% logd stop obgrad, poppinglipput)		300	500	μs	
Transient Response Deviation <sup>®</sup>	23% load step change, norminal input v	25% load step change, nominal input voltage ±5 ±8			±8	%
Temperature Coefficient	Full load				±0.03	%/℃
Diamla 9 Maiae®	20MHz bandwidth, nominal input, full	Vo1		70	150	
Ripple & Noise®	load	Vo2		100	150	mV p-p
Over-current Protection <sup>®</sup>			110		250	%lo
Short-circuit Protection <sup>®</sup>	Input voltage range		Hiccu	ıp, continuc	ous, self-rec	overy

Note:

1)Load regulation for 0%-100% load is ±5%;

2 Dynamic load only refer to Vo1;

③The "parallel cable" method is used for Ripple and Noise test, please refer to DC-DC Converter Application Notes for specific information;
 ④Dual output with balanced-load;

(5) Both Vo1/Vo2 go into hiccup protection when one of them short circuit; Vo2 is allowed to be short circuit only under condition of Vo1 with load range from 10%-100%; Vo1 could be short circuit if Vo2 with load range from 0%-100%.

General Specification	on				
ltem	Operating Conditions	Min.	Тур.	Max.	Unit
	Input-output Electric Strength test for 1 minute with a leakage current of 1mA max.	3000			
Isolation	Output-output Electric Strength test for 1 minute with a leakage current of 1mA max.	1500			VDC
Insulation Resistance	Input-output resistance at 500VDC	1000			MΩ
Isolation Capacitance	Input-output capacitance at 100kHz/0.1V		1000		pF
Operating Temperature	See Fig. 1	-40		+85	ĉ
Storage Temperature		-55		+125	
Storage Humidity	Non-condensing	5		95	%RH
Pin Soldering Resistance Temperature	Soldering spot is 1.5mm away from case for 10 seconds			+300	°C
Vibration	on 10-150Hz, 5G, 0.75mm. along X, Y and			(, Y and Z	
Switching Frequency *	PWM mode		300		kHz
MTBF	MIL-HDBK-217F@25°C	1000			k hours
Note: *Switching frequency is me	asured at full load. The module reduces the switching frequency for light l	oad (below 50	%) efficiency	improvemer	it.

Mechanical Specifications				
Case Material	ack flame-retardant and heat-resistant plastic (UL94 V-0)			
Dimensions	27.40 x 9.50 x 12.00mm			
Weight	.4g (Тур.)			
Cooling method	Free air convection			

Electron	nagnetic C	Compatibility(EMC		
Frankright	CE	CISPR32/EN55032	CLASS B (see Fig.3-① for recommended circuit)	
Emissions	RE	CISPR32/EN55032	CLASS B (see Fig.3-① for recommended circuit)	
Immunity	ESD	IEC/EN61000-4-2	Contact ±4kV	perf. Criteria B
	RS	IEC/EN61000-4-3	10V/m	perf. Criteria A
	EFT	IEC/EN61000-4-4	±2kV (see Fig.3-2) for recommended circuit)	perf. Criteria B
	Surge	IEC/EN61000-4-5	line to line ±2kV (see Fig.3- $\textcircled{2}$ for recommended circuit)	perf. Criteria B
	CS	IEC/EN61000-4-6	3 Vr.m.s	perf. Criteria A

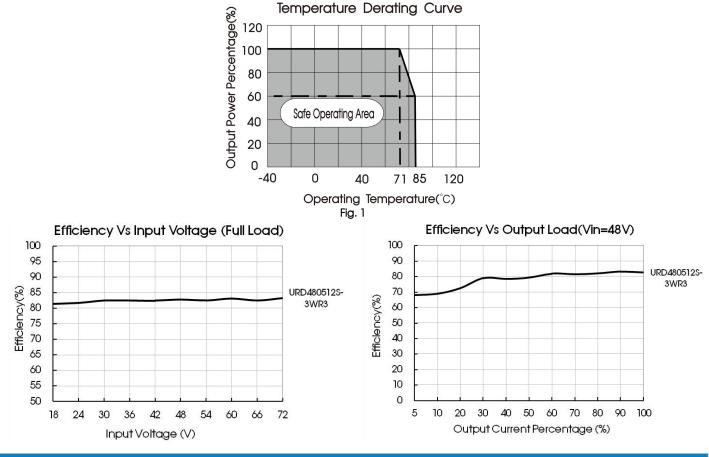
**MORNSUN**<sup>®</sup>

MORNSUN Guangzhou Science & Technology Co., Ltd.

2021.11.20-A/4 Page 2 of 5

# **MORNSUN**<sup>®</sup>

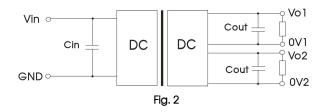
#### Typical Characteristic Curves



### **Design Reference**

#### 1. Typical application

All the DC-DC converters of this series are tested before delivery using the recommended circuit shown in Fig. 2. Input and/or output ripple can be further reduced by appropriately increasing the input & output capacitor values Cin and Cout and/or by selecting capacitors with a low ESR (equivalent series resistance). Also make sure that the capacitance is not exceeding the max. capacitive load value of the product.



Vout (VDC)	Cout	Cin
5	47µF/16∨	
12	22µF/25∨	100µF/100∨
24	22µF/50∨	

#### Note:

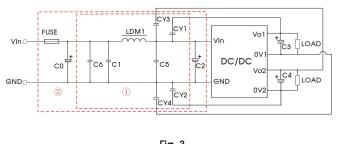
①CS Pin: By connecting a low ESR capacitor between this terminal and the pin-7, the output ripple and noise may be further improved. Generally, the capacitance is no greater than 47uF.

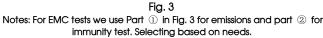


MORNSUN Guangzhou Science & Technology Co., Ltd.



### 2. EMC compliance circuit





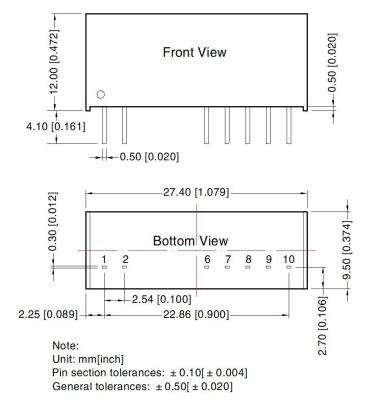
#### Parameter description

Vin: 48V					
Choose according to actual input current					
680µF/100V					
4.7µF/100∨					
330µF/100V					
Refer to the Cout in Fig.2					
22µH/0.6A					
InF/3kV					
2.2nF/3kV					

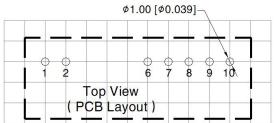
- 3. The products do not support parallel connection of their output
- 4. For additional information please refer to DC-DC converter application notes on www.mornsun-power.com

#### **Dimensions and Recommended Layout**

THIRD ANGLE PROJECTION 💮 🧲



**MORNSUN®** 



Note: Grid 2.54\*2.54mm

Pin-Out				
Pin	Mark			
1	GND			
2	Vin			
6	+Vo1			
7	0V1			
8	CS			
9	0V2			
10	+Vo2			

MORNSUN Guangzhou Science & Technology Co., Ltd.

2021.11.20-A/4 Page 4 of 5



Note:

- 1. For additional information on Product Packaging please refer to <u>www.mornsun-power.com</u>. Packaging bag number: 58200015;
- 2. The maximum capacitor load offered were tested at input voltage range and full load;
- 3. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated output load;
- 4. All index testing methods in this datasheet are based on company corporate standards;
- 5. We can provide product customization service, please contact our technicians directly for specific information;
- 6. Products are related to laws and regulations: see "Features" and "EMC";
- 7. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

## Mornsun Guangzhou Science & Technology Co., Ltd.

 Address: No. 5, Kehui St. 1, Kehui Development Center, Science Ave., Guangzhou Science City, Huangpu District, Guangzhou, P. R. China

 Tel: 86-20-38601850
 Fax: 86-20-38601272

 E-mail: info@mornsun.cn
 www.mornsun-power.com

**MORNSUN®** 

MORNSUN Guangzhou Science & Technology Co., Ltd.

2021.11.20-A/4 Page 5 of 5