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TDH341SCAN DFN package isolated CAN transceiver

Package

Features

- Ultra-small, ultra-thin, chip scale DFN package
- Compliant with ISO11898-2 standard
- Integrate 3.3V efficiently power supply
- I/O power supply range supports 3.3V and 5V microprocessors
- High isolation to 5000VDC
- Bus-Pin ESD protection up to 15kV(HBM)
- Baud rate up to 1Mbps
- -40V to +40V bus fault protection
- >25kV/us CMTI
- Low communication delay
- The bus supports maximum 110 nodes
- Industrial operating ambient temperature range: -40 $^\circ\!\mathrm{C}$ to +105 $^\circ\!\mathrm{C}$
- Meet EN62368 standards
- Moisture Sensitivity Level (MSL) 3

Applications

- · Industrial automation, control, sensors and drive systems
- Building and greenhouse environmental control(HVAC) automation
- Security system
- Transport
- Medical treatment
- Telecommunication
- CAN Bus standard such as CAN open, Device Net, NMEA2000, ARNIC825, ISO11783, CAN Kingdom, CAN aerospace

Functional Description

TDH341SCAN is a isolated CAN Bus transceiver, which is compliant with ISO11898-2 standard. Their logic side supports 3.3V and 5V logic level conversion.TDH341SCAN integrate 3.3 V efficiently power. The TDH341SCAN provide differential transmitting and receiving capability between the CANH protocol controller and the physical layer bus. It is capable of running at data rates of up to 1Mbps. The device has the function of series line, over-voltage(-40V to 40V), ground loss protection and thermal shutdown so that it is especially suitable for working in harsh environment.



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2024.08.07-A/2 Page 1 of 9

1	Hom	e	1
	1.1	Feature and Package	1
	1.2	Applications	1
	1.3	Functional Description	1
2	Pin c	onnection and Description	2
3	IC Re	elated Parameters	3
	3.1	Absolute Maximum Rating	3
	3.2	Recommended Operating Conditions	3
	3.3	Electrical Characteristics	4
	3.4	Transmission Characteristics	4
	3.5	Physical Characteristics	5

Pin Connection

GND1 S GND1 RXD TXD V10 GND1 Vcc GND1	$1 \bullet$ 2 3 4 5 6 7 8 9 10	TDH341SCAN	20 19 18 17 16 15 14 13 12	GND2 VISOIN NC CANH GND2 CANL NC GND2 VISOOUT
GND1 GND1	9		12	GND2

Note: All GND₁ pins are internally connected; All GND₂ pins are internally connected.

Function Table

Letter	Description
Н	High-Level
L	Low-Level
X	Unrelated
Z	High Impedance

Table	1.	Driver	Function	table	

Inp	outs	Out	Bus State	
TXD	S	CANH	CANL	Bus State
L	L (Or No Connection)	Н	L	Dominant
H (Or No Connection)	Х	Z	Z	Recessive
Х	н	Z	Z	Recessive

Table 2. I	Receiver	Function	table

VID=CANH-CANL	RXD	Bus State
Vid≥0.9V	L	Dominant
0.5 <vid<0.9v< td=""><td>Uncertainty</td><td>Uncertainty</td></vid<0.9v<>	Uncertainty	Uncertainty
Vid≤0.5V	н	Recessive
Open	Н	Recessive

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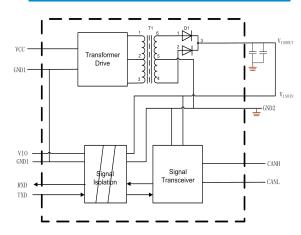
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2024.08.07-A/2

Page 2 of 9

4 Characteristic Curve	5
4.1 Typical Performance Curve	5
4.2 Parameter Measurement Information	6
5 Product Working Description	7
6 Application Circuit	7
7 Suggestions for Power Supply	8
8 Order Information	8
9 Package Information	8
10 Tape & Reel Information	9

Internal Block Diagram



Pin Number	Pin Name	Pin Functions
1	GND ₁	Ground(Logic side)
2	S	Ground Pin. In normal applied, this pin should connect to ground GND ₁ .
3	GND ₁	Ground(Logic side)
4	RXD	Receiver output pin.
5	TXD	Driver input pin.
6	V _{IO}	Isolation power supply pin. By using 0.1uF ceramic capacitance ground GND ₁ .
7	GND ₁	Ground(Logic side)
8	V _{CC}	Power supply pin. By using 1uF ceramic capacitance ground GND1.
9	GND ₁	Ground(Logic side)
10	GND ₁	Ground(Logic side)
11	GND ₂	Ground (Bus side)
12	VISOOUT	Insulation power output. By using 1uF ceramic capacitance ground GND ₂ . The pin needs to be connected to pin19 in application.
13	GND ₂	Ground (Bus Side)
14	NC	No connect
15	CANL	CANL pin
16	GND ₂	Ground (Bus side)
17	CANH	CANH pin
18	NC	No connect
19	VISOIN	Insulation power input. By using 0.1uF ceramic capacitance ground GND ₂ . The pin needs to be connected to pin12 in application.
20	GND ₂	Ground (Bus side)

Absolute Maximum Ratings

General test conditions: Free-air, normal operating temperature range (unless otherwise specified).

Parameters	Unit
Supply voltage,Vcc	-0.3V to +3.5V
Digital input voltage TXD, RXD	-0.3V to +5.5V
Bus voltage (CANH, CANL)	-40 to 40V
Receiver output current	-15 to 15mA
Operating temperature range	-40°C to +105°C
Storage temperature range	-50°C to +130°C
Reflow soldering temperature	Peak temp. ≤250°C, maximum duration ≤60s at 217°C. Please also refer to IPC/JEDEC J-STD-020D. 3.

Important: Exposure to absolute maximum rated conditions for an extended period may severely affect the device reliability, and stress levels exceeding the "Absolute Maximum Ratings" may result in permanent damage.

Recommended Operating Conditions

	Paramete	Min.	Nom.	Max.	Unit		
Vcc	Pov	wer supply	3.15	3.3	3.45	V	
V _{IO}	Power su	ipply(Logic Side)	2.375		5.5	V	
VI or VIC	Voltage at any bus	terminal (differential mode)	-12		12	V	
Vін	High-level i	2			V		
VIL	Low-level i			0.8	V		
	High-level output current	Driver	Driver	-70			
Іон		Receiver V _{CC} =3.3V	-4			mA	
		Driver			70		
IOL	Low-level output current	Receiver V _{CC} =3.3V			4	mA	
TA	Operating temperature range		-40		105	°C	
Icc	Recessive mode current			20	35	mA	

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Icc	Working current	Vcc= 3.3V,RL= 60Ω;TXD signal: f=500kHz;Duty=50%		50	65	mA
	Sig	naling rate	0		1000	kbps

Electrical Characteristics

General test conditions and V_{cc}=V_{I0}= 3.3V, Ta = 25 $^{\circ}$ C (unless otherwise specified).

	Parameters	Conditions	Min.	Nom.	Max.	Unit	
Driver							
Dominant CANH output voltage			2.75	3.5	4.5		
Vo(d)	Dominant CANL output voltage	Figure 8 V_{TXD} = 0 V, RL = 60 Ω	0.5	1.5	2.25	V	
VO(R)	Recessive bus voltage	Figure 8 V _{TXD} = 2 V, RL= 60 Ω	2	2.5	3	V	
VOD(D)	Differential output voltage	Figure 8 V _{TXD} = 0 V, t < $t_{to(dom)TXD}$, V _{CC} =3.15 V to 3.45 V, RL = 50 to 65 Ω	1.5		3	V	
		Figure 8 V_{TXD} = 3.3 V, RL = 60 Ω	-0.12		0.012		
VOD(R)	Recessive differential output voltage	V _{TXD} = 3.3 V, No load	-0.5		0.05	05 V	
Ін	TXD High-level input current	V _{TXD} =2 V			20	uA	
lı∟	TXD Low-level input current	V _{TXD} =0.8 V	-20			uA	
RTXD	Internal TXD Pull up Resistor			9.1		kΩ	
Receiver				I I			
VIT+	Positive-going input threshold voltage	F : 44		750	900	mV	
VIT-	Negative-going input threshold voltage	- Figure 11	500	650		mV	
Vhys	Hysteresis voltage (V _{IT+} - V _{IT-})			120		mV	
		Іон = –4 mA, Figure 9	V _{IO} – 0.4	V _{IO} - 0.2		- v	
Vон	High-level output voltage	Іон = –20 uA, Figure 9	V10-0.1				
		Io∟ = 4 mA, Figure 9		0.2	0.4		
Vol	Low-level output voltage	IoL = 20 uA, Figure 9		0	0.1	V	
Сі	Input capacitance to ground (CANH or CANL)	V _{TXD} = 3.3 V, VI = 0.4 sin (4E6 π t) + 2.5 V		13		pF	
CID	Differential input capacitance	V _{TXD} = 3.3 V, VI = 0.4 sin (4E6 π t)		5		pF	
Rid	Differential input resistance	V _{TXD} =3.3V	15	30	40	kΩ	
Rin	Input resistance (CANH or CANL)	V _{TXD} =3.3 V	30		80	kΩ	
R I(m)	Input resistance matching: [1 - R _{IN(CANH)} / R _{IN(CANL)}] × 100%	VCANH = VCANL	-3%	0%	3%		
500	LIDM	CANH, CANL pin to GND			±15	kV	
ESD	HBM	Other pins			±2	kV	
EFT	IEC61000-4-4: Perf. Criteria B	CANH, CANL and GND			±2	kV	
Surge	IEC61000-4-5 : Perf. Criteria B	CANH, CANL and GND(Common Mode)			±2	kV	
	Isolation voltage				5000	VDC	
	Insulation resistance		1			GΩ	
	Isolation capacitor			3		pF	
CMTI	Common Mode Transient Immunity	V_{TXD} = V_{CC} or 0 V, V_{CM} = 1 kV, transient magnitude = 800 V	25			kV/us	

Transmission Characteristics General test conditions and $V_{cc}=V_{10}=3.3V$, Ta = 25[°]C (unless otherwise specified).

Parameters		Conditions	Min.	Nom.	Max.	Unit
tonTxD	Propagation delay TXD On to bus active			80	150	ns
t _{offTxD}	Propagation delay TXD Off to bus inactive	RL = 60 Ω, CL = 100 pF, see Figure 10 and Figure 12		80	200	ns
t _{onRxD}	Propagation delay RXD On to receiver active			60	300	ns
$t_{\rm offRxD}$	Propagation delay RXD Off to receiver inactive			60	250	ns

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2024.08.07-A/2 Page 4 of 9

Parameters	Value	Unit
Weight	0.9(Тур.)	g

Typical Performance Curves

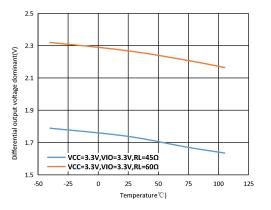


Figure 1. Drive differential output voltage dominant VS Temperature

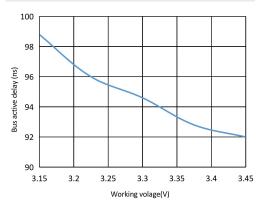


Figure 3. Propagation delay from TXD On to bus active VS Working voltage

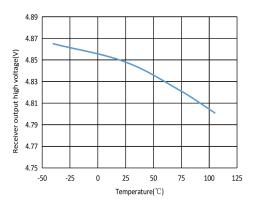


Figure 5. Receiver output high voltage VS Temperature

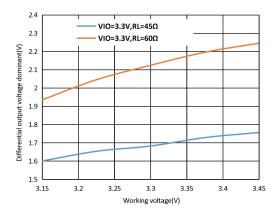


Figure 2. Drive differential output voltage dominant VS Working voltage

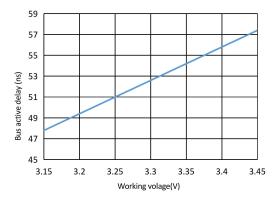


Figure 4. Propagation delay from TXD Off to bus inactive VS Working voltage

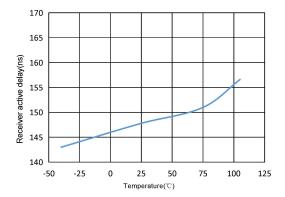


Figure 6. Receiver active delay VS Operating Temperature



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2024.08.07-A/2 Page 5 of 9

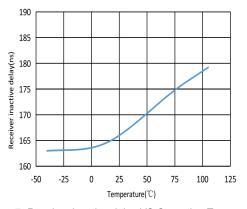
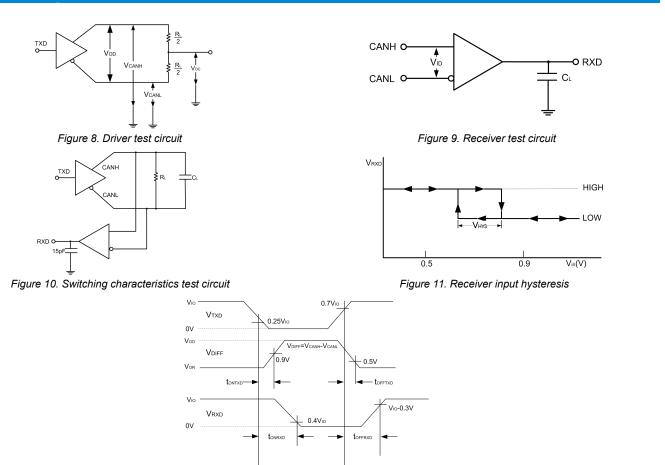


Figure 7. Receiver inactive delay VS Operating Temperature

Test Circuits





Detailed Description

TDH341SCAN is a CAN of a style of separation transceiver with the ability of differential signal transmission between the bus and CAN protocol controller, it the inner integration insulate DC/DC power supply. which is compliant with ISO11898-2 standard.

Short-circuit protection: TDH341SCAN has current-limiting protection to prevent the drive circuit from short-circuiting to positive and negative supply voltages. The power dissipation increases when a short circuit occurs. The short-circuit protection function protects the driver stage from damage.

Over-temperature protection: TDH341SCAN has over-temperature protection. When the junction temperature exceeds 160°C, the current in the driver stage will decrease. Because the drive tube is the primary energy consuming component, current reduction can reduce power consumption and reduce chip temperature. At the same time, the rest of the chip remains functional.



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2024.08.07-A/2 Page 6 of 9

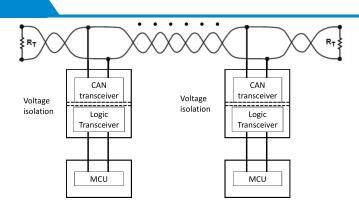
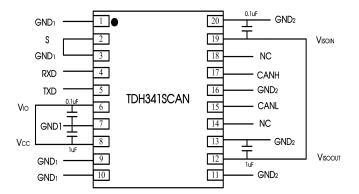
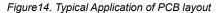


Figure 13. Typical Application Circuit





In General, Vcc and VIo can be shorted (Figure 14) .If the controller doesn't support 3.3V signal input, it can power 5V for VIo. When the module is working normally, please connect the S pin to ground GND1

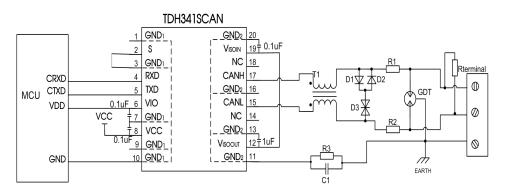


Figure 15. Port protection circuit for harsh environments

Recommended components and values:

Component	Recommended part, value	Component	Recommended part, value		
R3	1ΜΩ	D1、D2	1N4007		
C1	1nF, 2kV	D3	SMBJ30CA		
T1	ACM2520-301-2P	R _{terminal}	120 Ω		
GDT	B3D090L	R1、R2	2.7Ω/2W		

When the module is used in applications with harsh environment, it can be susceptible to large energy like lightning strike, etc. in which case, it is essential to add an adequate protection circuit to the CAN signal ports to protect the system from failure and maintain a reliable bus communication. Figure 15 provides a recommended protection circuit design for high-energy lightning surges, with a degree of protection related to the selected protection device. Parameter description lists a set of recommended circuit parameters, which can be adjusted according to the actual application situation. Also, when using the shielded cable, the reliable single-point grounding of the shield must be achieved.

Note: The recommended components and values is a general guideline only and must be verified for the actual user's application. We recommended using PTC's for R1 and R2 and to use fast recovery diodes for D1 and D2.



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2024.08.07-A/2 Page 7 of 9

Using Suggests

Power isolation V_{ISOOUT} need through a series of capacitors connected to the output pin V_{ISOIN}, the power supply is not recommended for other purposes, otherwise it may cause the bus voltage did not meet the requirements of communication, causes the communication failure.
Hot-swap is not supported.

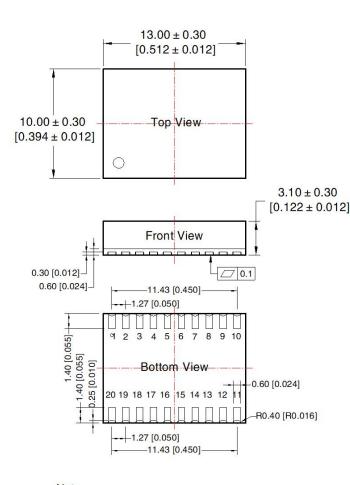
③ If the external input of TXD is insufficient, the pull-up resistor should be added according to the situation.

④ Refer to IPC 7093 for the welding process design of this product. For detailed operation guidance, please refer to Hot Air Gun Welding Operation Instruction for DFN Package Product or Welding Operation Instruction for DFN Package Product.

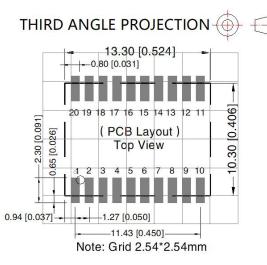
Ordering Information

Part number	Part number Package		Product marking	Tape & Reel	
TDH341SCAN DFN		20	TDH341SCAN	300/REEL	

Package Information



Note: Unit: mm[inch] Pin diameter tolerances: ± 0.10[±0.004]

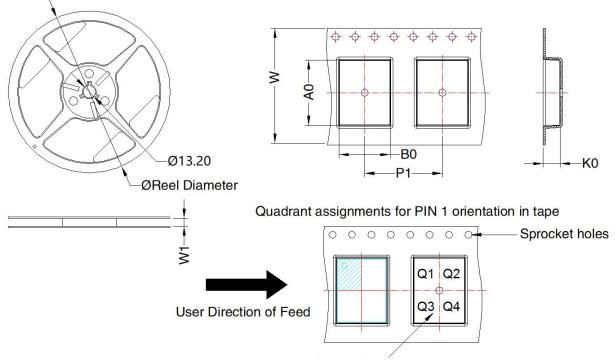


Pin-Out								
Pin	Mark	Pin	Mark					
1	GND ₁	11	GND ₂					
2	S	12	VISOOUT					
3	GND ₁	13	GND ₂					
4	RXD	14	NC					
5	TXD	15	CANL					
6	V _{IO}	16	GND ₂					
7	GND ₁	17	CANH					
8	8 V _{CC}		NC					
9	GND ₁	19	V _{ISOIN}					
10	GND ₁	20	GND ₂					

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2024.08.07-A/2 Page 8 of 9



Pocket Quadrants

Device	Package Type	Pin	MPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TDH341SCAN	DFN 10x13	20	300	180.0	24.4	13.52	10.52	3.5	16.0	24.0	Q1

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2024.08.07-A/2 Page 9 of 9