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TD041S485H-A DFN package isolated RS485 Transceiver

Features

- Ultra-small, ultra-thin, chip scale DFN package
- Compliant with TIA/EIA-485A standard
- I/O power supply range supports 3.3V and 5V microprocessors
- High isolation to 3750Vrms
- Bus-Pin ESD protection up to 15kV(HBM)
- Communication rate is up to 250kbps
- > 25kV/µs CMTI
- · Automatic send and receive data function
- 1/8 unit load—up to 256 nodes on a bus
- Bus fail-safe
- Bus driver short circuit protection
- Industrial operating ambient temperature range: -40 $^\circ C$ to +105 $^\circ C$
- Moisture Sensitivity Level (MSL) 3

Applications

- Industrial Automation
- Building Automation
- Smart Electricity Meter
- Remote Signal Interaction, Transmission

Functional Description



Package

TD041S485H-A is a half-duplex enhanced transceiver designed for RS – 485 data bus networks, which is fully compliant with TIA/EIA-485A standard. In addition, the product features an automatic switching function, that no longer requires the need to pass through the node to send and receive control signals, which to a certain extent, reduces the design complexity. Their logic side supports 3.3V and 5V logic level conversion. Receivers have an exceptionally high input impedance, which places only 1/8 of the standard load on a shared bus and up to 256 transceivers .High bus transmission rate of 250kbps.

TD041S485H-A reliability design of A, B pin is emphasized, including driver output over current protection and enhanced ESD design. The ESD protection level of A, B pin can be up to 15kV (Human Body Model).



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Pin Connection

VDD1	1	16	VDD2
GND1	2	15	GND2
RXD	3	14	NC
NC		13	В
NC	5	12	А
TXD	6	11	NC
GND1	7	10	GND2
GND1	8	9	GND2

Note: All GND₁ pins are internally connected; All GND₂ pins are internally connected.

Function Table

Letter	Description
Н	High-Level
L	Low-Level
Х	Unrelated
Z	High Impedance
NC	No Connection

Table 1. Driver Function Table

Power		Input	Out	put
V _{DD1}	V _{DD2}	TXD	А	В
On	On	Н	Н	L
On	On	L	L	Н

Table 2. Receiver Function Table					
Power		Input	Output		
V _{DD1}	V _{DD2}	A-B (V)	RXD		
On	On	>-0.01	Н		
On	On	<-0.2	L		
On	On	-0.2 < A - B < -0.01	Uncertainty		
On	On	OPEN	Н		
Off	Off	Х	L		



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Pin Number	Pin Name	Pin Functions
1	V _{DD1}	Power Supply(Logic side)
2	GND1	Ground(Logic side)
3	RXD	Receiver Output Data
4	NC	No Connect
5	NC	No Connect
6	TXD	Driver Input
7	GND1	Ground(Logic side)
8	GND1	Ground(Logic side)
9	GND ₂	Ground (Bus Side)
10	GND ₂	Ground (Bus Side)
11	NC	No Connect
12	A	RS485 Bus A Line
13	В	RS485 Bus B Line
14	NC	No Connect
15	GND ₂	Ground (Bus Side)
16	V _{DD2}	Power Supply (Bus Side)

Absolute Maximum Ratings

General test conditions: Free-air, normal operating temperature range (unless otherwise specified).

PARAMETERS	UNIT
V _{DD1}	-0.5 V to +7 V
V _{DD2}	-0.5 V to +6 V
Digital Input Voltage (TXD)	-0.3V to +6V
Digital Output Voltage (RXD)	-0.3V to +6V
Driver Output / Receiver input Voltage	-8 V to +13 V
Operating Temperature Range	-40°C to +105°C
Storage Temperature Range	-50°C to +125°C
Reflow Soldering Temperature	Peak temp. ≤260 °C, maximum duration ≤60s at 217 °C. Please also refer to IPC/JEDEC J-STD-020D.3.

Important: Exposure to Absolute Maximum Rated conditions for an extended period may severely affect the device reliability, and stress levels exceeding the "Absolute Maximum Ratings" may result in permanent damage. All voltage values are referenced to the reference ground (GND).

Recommended Operating Conditions

Recommended	Operating Conditions	Min.	Тур.	Max.	Unit
V _{DD1}	Logic Power Supply	2.375	3.3	5.5	
V _{DD2}	Bus Power Supply	4.5	5	5.5	
V _{IH}	High-level input voltage(TXD)	2		V _{DD1}	V
VIL	Low-level input voltage(TXD)	0		0.8	-
V _{ID}	Differential input voltage	-7		+12	
	Signaling rate			250	kbps

Electrical Characteristics

General test conditions and $V_{DD1}=V_{DD2}=5V$, Ta = 25°C (unless otherwise specified).

PARAMETERS		CONDITIONS	Min.	Тур.	Max.	Unit
DRIVER						
V _{OD}	Differential Driver Output	R∟ = ∞, Figure 6	1.5			V
los	Output Short-Circuit Current	-7V≤V _A or V _B ≤12V		±110	±250	mA

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VIH	Input High Voltage	TXD	2			V
VIL	Input Low Voltage	TXD			0.8	V
RECEIVE	R					
V _{IT(+)}	Positive Differential Input Threshold Voltage	$-7 \text{ V} \le \text{V}_{\text{A}} \text{ or } \text{V}_{\text{B}} \le +12 \text{ V}$			-10	mV
VIT(-)	Negative Differential Input Threshold Voltage	$-7 \text{ V} \le \text{V}_{A} \text{ or } \text{V}_{B} \le +12 \text{ V}$	-200			mV
V _{hys}	Hysteresis Voltage (V _{IT+} – V _{IT-})	$-7 \text{ V} \le \text{V}_{\text{A}} \text{ or } \text{V}_{\text{B}} \le +12 \text{ V}$		20		mV
R _{ID}	Differential Input Resistance(A,B)	$-7 \text{ V} \le \text{V}_{\text{A}} \text{ or } \text{V}_{\text{B}} \le +12 \text{ V}$	96			kΩ
Vон	RXD Output High Voltage	I_{OUT} = 4 mA, V_A – V_B = 0.2 V	V _{DD1} - 0.4	V _{DD1} - 0.2		V
V _{OL}	RXD Output Low Voltage	$I_{OUT} = -4 \text{ mA}, V_A - V_B = 0.2 \text{ V}$			0.4	V
Supply an	d Protection					
IDD1	Supply Current(Logic side)	No load			3	mA
I _{DD2}	Supply Current(Bus side)	No load			13	mA
I _{DD}	Max. Operating Current	No load			25	mA
		A, B and GND			±15	kV
ESD	пым	Other pins			±2	kV
	Contact	A, B and GND			±4	kV
EFT	IEC61000-4-4: Perf. Criteria B	A, B and GND			±2	kV
Surge	IEC61000-4-5 : Perf. Criteria B	A, B and GND(Common Mode)			±2	kV
V _{I-O}	Isolation Test	Leakage current <1mA.			3750	Vrms
R _{I-0}	Insulation Resistance	At 500VDC	1000			MΩ
CI-O	Isolation capacitor			3		pF
СМТІ	Common Mode Transient Immunity	TXD = V_{DD1} or 0 V, V_{CM} = 1 kV, transient magnitude = 800 V	25			kV/µs

Transmission CharacteristicsGeneral test conditions and $V_{DD1}=V_{DD2}=5V$, Ta = 25°C (unless otherwise specified).

PARAMETERS		CONDITIONS	Min.	Тур.	Max.	Unit
	Maximum Data Rate				250	kbps
DRIVER	DRIVER					
t _{PLH} , t _{PHL}	Propagation Delay				600	ns
tr, t _f	Rise/Fall Time	No load, $C_{L1} = C_{L2} = 100 \text{ pF}$, Figure7			100	ns
RECEIVER						
t _{PLH} , t _{PHL}	Propagation Delay			60	150	ns
t _{skew}	Differential Skew (T _{PLH} - T _{PHL})	$C_L = 15 \text{ pF}, \text{ Figure8}$			20	ns
t _r , t _f	Bus rise/fall time			25		ns

Physical Specifications

PARAMETERS	Value	Unit
Weight	0.4(Typ.)	g



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Figure1. Unloaded Supply Current vs. Temperature



Figure3. Receiver Output High Voltage vs. Temperature, VDD1=5V, VDD2=5V



igure 5. Receiver Progation Delay vs. Temperature





Figure 6. Driver Test Circuit



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Figure 2. Transmit Current vs. Temperature, VDD1=5V, VDD2=5V



Figure4. Driver Progation Delay vs. Temperature

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Detailed Description

TD041S485H-A is an advanced RS-485 transceivers. They each contain one driver and one receiver. These devices feature a fail-safe circuitry that guarantees a high receiver output voltage when the receiver inputs are either open, shorted or when they are connected to a terminated transmission line with all drivers disabled. TD041S485H-A operates with a two power supply. Their logic side supports 3.3V and 5V logic level conversion. The whole machine can monitor the overall working state of the module and limit the output high current to prevent the bus from overload or short circuit causing unrecoverable damage to the transceiver.

Receiver input filter: TD041S485H-A receiver have an integrated input filter which enhances noise immunity of the high-speed differential signals. The receiver propagation delay increases due to this filtering.

Bus fail-safe: Ordinary RS485 bus receivers will be in an indeterminate state when -200mV< A - B <-10mV. This situation can occur whenever the data bus is not being actively driven. The advanced Fail-safe feature of the TD041S485H-A guarantees a high receiver output voltage if the receiver's differential inputs are either shorted, open circuit, or if they are connected to a termination resistor.

The TD041S485H-A receiver thresholds are very precise, and the offset between threshold voltage and ground has a margin of at least 10mV. This guarantees that the receiver output is a high voltage even the input differential is zero volts, thus maintaining compliance with the EIA/TIA-485 standard.

Load abilities on the bus (256 nodes): The standard receiver input impedance of RS-485 is $12k\Omega$ (1 unit load). A standard RS485 driver can drive at least 32 unit loads. The TD041S485H-A transceiver is designed to 1/8th of the standard unit load and the input impedance is higher than $96k\Omega$, hence allowing up to 256 unit loads. The TD041S485H-A can work combined with other standard RS485 that use the smaller amount of unit loads.

Driver output protection: The device prevents excessive output current caused by fault conditions or driver short circuit. A driver current limit on the output stage provides and ensures immediate protection against short circuits over the entire common mode voltage range.

Automatic switching function: The product integrates an automatic switching unit so that no longer requires the need to pass through the node to send and receive control signals, which to a certain extent, reduces the design complexity.

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Figure 9. Typical application circuit



Figure 10. Port protection circuit for harsh environments

Recommended components and values:

Component	Recommended part, value	Component	Recommended part, value
R3	1ΜΩ	R1、R2	2.7Ω/2W
C1	1nF, 2kV	D1、D2	1N4007
T1	ACM2520-301-2P	D3	SMBJ8.5CA
GDT	B3D090L		

As the modules internal A / B lines come with its own ESD protection, which generally satisfy most application environments without the need for additional ESD protection devices. For harsh and noisy application environments such as motors, high voltage/current switches, lightning and similar however, we recommended that the user protects the module' s A / B lines with additional measures and external components such as TVS tube, common mode inductors, Gas discharge tube, shielded twisted pair of wires with the same single network Earth point. Figure 10 shows our recommended circuit diagram for such type of applications with components and values given in the table above. This recommendation is for reference only and may have to be adapted accordingly with appropriate component values in order to match the actual situation and application.

Note 1: Select the R_{terminal} according to the actual application.

Note 2: When using the port protection circuit, you need to slow down the baud rate.

Using Suggests

① Hot-swap is not supported.

② If the external input of TXD is insufficient, the pull-up resistor should be added according to the situation.

③ Refer to IPC 7093 for the welding process design of this product. For detailed operation guidance, please refer to Hot Air Gun Welding Operation Instruction for DFN Package Product or Welding Operation Instruction for DFN Package Product.



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Ordering Information

Part number	Package	Number of pins	Product Marking	Tape & Reel	
TD041S485H-A	DFN	16	TD041S485H-A	500/REEL	

Package Information



Note: Unit: mm[inch] General tolerances: ± 0.10[± 0.004]



Note: Grid 2.54*2.54mm

	Pin-Out							
Pin	Mark	Pin	Mark					
1	V _{DD1}	9	GND ₂					
2	GND1	10	GND ₂					
3	RXD	11	NC					
4	NC	12	А					
5	NC	13	В					
6	TXD	14	NC					
7	GND ₁	15	GND₂					
8	GND ₁	16						

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Device	Package Type	Pin	MPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TD041S485H-A	DFN 10x10	16	500	180.0	24.4	10.44	10.44	2.0	16.0	24.0	Q2

Pocket Quadrants

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