Wide input voltage Non-isolated and regulated single output



FEATURES

- High efficiency up to 95%
- No-load input current as low as 0.2mA
- Operating ambient temperature range: -40°C to +85°C
- Output short-circuit protection
- SMD package

K78_T-500R3-TR series are high efficiency switching regulators. The converters feature high efficiency, low loss and short circuit protection in a compact SMD package. These products are widely used in applications such as industrial control, instrumentation and electric power.

0 - 4641	Part No.	Input Voltage (VDC)*	· CHIPHI			Capacitive Load (µF)	
Certification	Pari No.	Nominal (Range)	Voltage (VDC)	Current (mA) Max.	Efficiency (%) Typ. Vin Min. / Vin Max.	Max.	
	K7801T-500R3-TR	12 (4.75-28)	1.5	500	76/67	680	
	K7802T-500R3-TR	12 (4.75-32)	2.5	500	81/74	680	
	K7803T-500R3-TR	24 (4.75-36)	3.3	500	86/80	680	
ENI/DO ENI	K7805T-500R3-TR	24 (6.5-36)	5	500	90/84	680	
EN/BS EN	K78X6T-500R3-TR	24 (8-36)	6.5	500	92/87	680	
	K7809T-500R3-TR	24 (12-36)	9	500	93/90	680	
	K7812T-500R3-TR	24 (15-36)	12	500	94/91	680	
	K7815T-500R3-TR	24 (19-36)	15	500	95/93	680	

Input Specifications Item **Operating Conditions** Min. Max. Unit Тур. No-load Input Current 0.2 1.5 mΑ Reverse Polarity at Input Avoid / Not protected Input Filter Capacitance filter Module on Ctrl pin open or pulled high (TTL 3.2-8VDC) Ctrl* Module off Ctrl pin pulled low to GND (0-0.8VDC) Input current when off 30 100 μΑ Note: *The Ctrl pin voltage is referenced to input GND.

Output Specifications									
Item	Operating Conditions	Operating Conditions			Max.	Unit			
\/alterna A a a maray	Full load, input	1.5/2.5/3.3 VDC output		±2	±4				
Voltage Accuracy	voltage range	Other output		±2	±3	%			
Linear Regulation	Full load, input voltage	Full load, input voltage range			±0.4				
Logal Dogwalation	Nominal input	1.5/2.5/3.3/5 VDC output		±0.6					
Load Regulation	voltage, 10% -100% load	Other output		±0.3		%			

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DC/DC Converter K78_T-500R3-TR Series



Discuss O Nichort	20MHz bandwidth,	1.5/2.5/3.3 VDC output, 20% -100% load		20	50	
Ripple & Noise*	nominal input voltage	Other output, 10% -100% load	-	20	50	mVp-p
Temperature Coefficient	Operating temperature			±0.03	%/℃	
Transient Response Deviation	Name and in part to college	OFW Is and store above as		50	200	mV
Transient Recovery Time	Nominal input voltage,	25% load siep change		0.2	1	ms
Short-circuit Protection	Nominal input voltage		Continuous, self-recovery			
Vadj	input voltage range			±10		%Vo
Note: *	'	'				

^{2.} With light loads at or below 20%, Ripple & Noise for 1.5/2.5/3.3V output parts increases to 100mVp-p max. and a load below 10% for 5V/6.5V/9V/12V/15V output parts levels increase to 150mVp-p max.

General Specifications							
Item	Operating Conditions		Min.	Тур.	Max.	Unit	
Operating Temperature	See Fig. 1	-40	_	+85	°C		
Storage Temperature		-55	-	+125	C		
Storage Humidity	Non-condensing	5	-	95	%RH		
Reflow Soldering Temperature		Peak temperature ≤245°C, duration ≤60s max. over 217°C. Also refer to IPC/JEDEC J-STD-020D.1.					
O ditable a Francisco as	Full location and an arrival	K7801T-500R3-TR		370	_		
Switching Frequency	Full load, nominal input	Other output		700	_	kHz	
MTBF	MIL-HDBK-217F@25°C	2000		-	k hours		
Moisture Sensitivity Level (MSL)*	IPC/JEDEC J-STD-020D.1	Level 1					
Note: * For actual application, please re	fer to IPC/JEDEC J-STD-020D.1.						

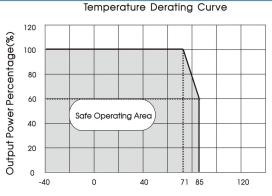
Mechanical Specifications						
Case Material Black plastic; flame-retardant and heat-resistant (UL94V-0)						
Dimensions	15.24 x11.40 x 8.25mm					
Weight	1.5g (Typ.)					
Cooling Method Free air convection						

Electromagnetic Compatibility (EMC)									
Emissions	CE	CISPR32/EN55032	CLASS B (see Fig. 4-2) for recommended circuit)						
	RE	CISPR32/EN55032	CLASS B (see Fig. 4-2) for recommended circuit)						
	ESD	IEC/EN 61000-4-2	Contact ±4kV	perf. Criteria B					
	RS	IEC/EN 61000-4-3	10V/m	perf. Criteria A					
Immunity	EFT	IEC/EN 61000-4-4	±1kV (see Fig. 4-① for recommended circuit)	perf. Criteria B					
	Surge	IEC/EN 61000-4-5	line to line ±1kV (see Fig. 4-① for recommended circuit)	perf. Criteria B					
	CS	IEC/EN 61000-4-6	3Vr.m.s	perf. Criteria A					

^{1.} The "parallel cable" method is used for Ripple and Noise test, please refer to DC-DC Converter Application Notes for specific information;

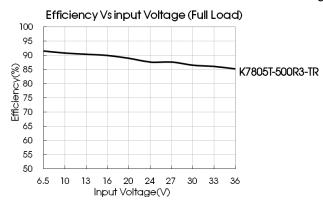
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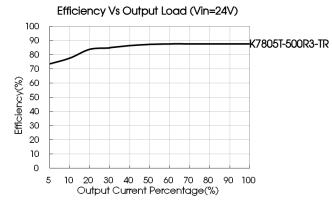
Typical Characteristic Curves

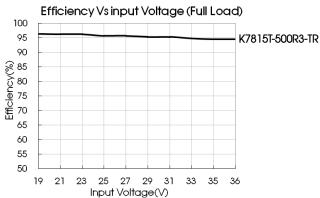


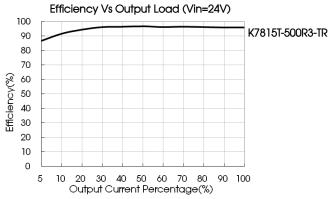
Operating Temperature(°C)

Fig. 1









Design Reference

1. Typical application

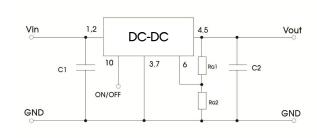


Fig. 2 Typical application circuit

Part No.	C1 (ceramic capacitor)	C2 (ceramic capacitor)	Ra1/Ra2 (Vadj resistance)
K7801T-500R3-TR		22µF/10V	
K7802T-500R3-TR		22µF/10V	
K7803T-500R3-TR		22µF/10V	
K7805T-500R3-TR	10µF/50V	22µF/16V	Refer to Vadj resistance
K78X6T-500R3-TR	Τυμε/300	22µF/16V	calculation
K7809T-500R3-TR		22µF/25V	- Gai Gai Gii Gii
K7812T-500R3-TR		22µF/25V	
K7815T-500R3-TR		22µF/25V	

table 1



Note

- 1. The required C1 and C2 capacitors must be connected as close as possible to the terminals of the module;
- 2. Refer to Table 1 for C1 and C2 capacitor values. For certain applications, increased values and/or tantalum or low ESR electrolytic capacitors may also be used instead;
- 3. Converter cannot be used for hot swap and with output in parallel;
- 4. To further reduce the output ripple and noise, we suggested the use of a "LC" filter at the output terminals, with an inductor value (L) of 10µH-47µH.

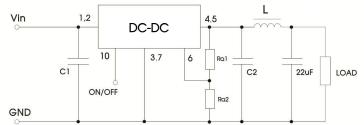


Fig. 3 External "LC" output filter circuit diagram

2. EMC Compliance circuit

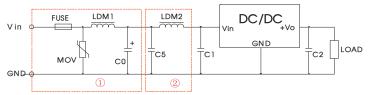
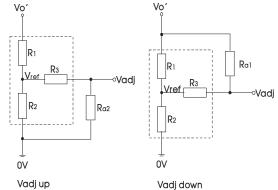


Fig.4 Recommended compliance circuit

FUSE	MOV	LDM1	C0	C1/C2	C5	LDM2
Select fuse value according to actual input current	S20K30	82µH	680µF /50V	Refer to table 1	4.7µF /50V	12µH

Note: Part ① in Fig. 4 shows Immunity compliance filter and part ② filter for Emission compliance; depending on requirement both filters ① and ② can be used in series as shown.

3. Trim Function for Output Voltage Adjustment (open if unused)



Calculating Trim resistor values:

up:
$$R_{a2} = \frac{aR_2}{R_2 - a}$$
 -R3 $a = \frac{Vref}{Vo' - Vref}$ R

down: $R_{a1} = \frac{aR_1}{R_1 - a}$ -R3 $a = \frac{Vo' - Vref}{Vref}$ R

Ra1 \times Ra2 = Trim Resistor value; Vo' =desired output voltage. a = self - defined parameter;

Fig. 5 Circuit diagram of Vadj up and down (dashed line shows internal part of module)

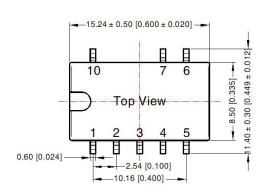
Vout(V)	R1(k Ω)	R2(k Ω)	R3(k Ω)	Vref(V)
1.5	7.5	7.5	15	0.75
1.8	35.7	26.29	100	0.765
2.5	27	11.858	51	0.765
3.3	33	9.9	47	0.765
5	75	13.5	75	0.765
6.5	75	10	51	0.765
9	51	4.7	27	0.765
12	75	5.1	27	0.765
15	82	4.423	27	0.765

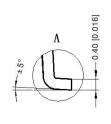
Note: The 1.5V model's output voltage can only be adjusted up (Vadj up) and cannot be adjusted to a lower voltage (Vadj down is not applicable).

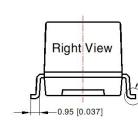


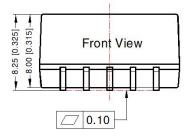
4. For additional information please refer to DC-DC converter application notes on www.mornsun-power.com

Dimensions and Recommended Layout





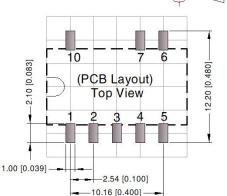




Note: Unit: mm[inch]

Pin section tolerances: $\pm 0.10[\pm 0.004]$ General tolerances: $\pm 0.25[\pm 0.010]$

THIRD ANGLE PROJECTION

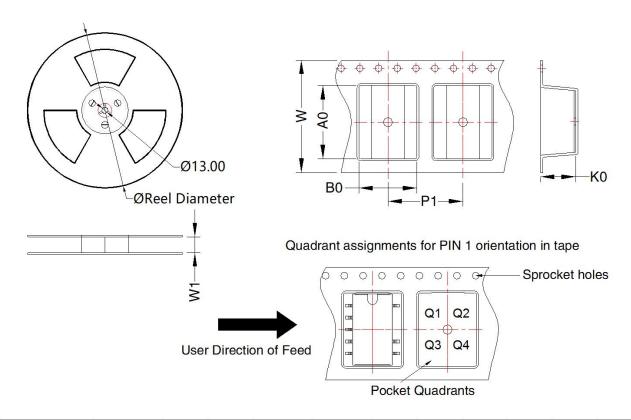


Note: Grid 2.54*2.54mm

Pin-Out							
Pin	Mark						
1	+Vin						
2	+Vin						
3	GND						
4	+Vout						
5	+Vout						
6	V adj						
7	GND						
10	Remote On/Off						

NC: Pin to be isolated from circuitry

Tape and Reel Info



	Device	Package Type	Pin	MPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
ł	K78_T-500R3-TR	SMD	8	300	330.0	24.5	15.74	12.8	8.5	20.0	24.0	Q1

Notes:

- 1. For additional information on Product Packaging please refer to www.mornsun-power.com. Packaging bag number: 58210058;
- 2. The maximum capacitive load offered were tested at nominal input voltage and full load;
- 3. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta= 25° C, humidity<75%RH with nominal input voltage and rated output load;
- 4. All index testing methods in this datasheet are based on our company corporate standards;
- 5. We can provide product customization service, please contact our technicians directly for specific information;
- 6. Products are related to laws and regulations: see "Features" and "EMC";
- 7. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

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