1W isolated DC-DC converter
Fixed input voltage, unregulated single output







Report Patent Protection RoH

# **FEATURES**

- Continuous short-circuit protection
- Operating ambient temperature range: -40  $^{\circ}\mathrm{C}$  to +105  $^{\circ}\mathrm{C}$
- High efficiency up to 80%
- SMD package
- I/O isolation test voltage 3k VDC
- Low ripple & noise
- Industry standard pin-out

F\_XT-1WR2 series are specially designed for applications where an isolated voltage is required in a distributed power supply system. They are suitable for: Pre-stage interference isolation, ground interference cancellation, pure digital circuits, voltage isolation conversion, general low-frequency analog circuits, relay drive circuits, etc.

		Input Voltage (VDC)	0	utput	Full Load	Capacitive Load (µF) Max.
Certification	Part No.	Nominal (Range)	Voltage (VDC)	Current (mA) Max./Min.	Efficiency (%) Min./Typ.	
	F0303XT-1WR2		3.3	303/30	65/69	
	F0305XT-1WR2	3.3 (2.97-3.63)	5	200/20	70/74	
	F0312XT-1WR2	(2.77 0.00)	12	84/9	72/76	
	F0503XT-1WR2		3.3	303/30	68/72	
	F0505XT-1WR2		5	200/20	76/80	
	F0506XT-1WR2	_	6	167/17	76/80	
	F0509XT-1WR2	5 (4.5-5.5)	9	111/12	76/80	
	F0512XT-1WR2	(4.0 0.0)	12	84/9	76/80	
	F0515XT-1WR2		15	67/7	76/80	
	F0524XT-1WR2		24	42/4	76/80	
EN	F1203XT-1WR2		3.3	303/30	68/72	220
	F1205XT-1WR2		5	200/20	74/78	
	F1209XT-1WR2	12	9	111/12	76/80	
	F1212XT-1WR2	(10.8-13.2)	12	84/9	76/80	
	F1215XT-1WR2		15	67/7	76/80	
	F1224XT-1WR2		24	42/4	76/80	
	F1515XT-1WR2	15 (13.5-16.5)	15	67/7	76/80	
	F2405XT-1WR2		5	200/20	76/80	
	F2409XT-1WR2	24	9	111/12	76/80	
	F2415XT-1WR2	(21.6-26.4)	15	67/7	76/80	
	F2424XT-1WR2		24	42/4	76/80	

Input Specifications					
Item	Operating Conditions	Min.	Тур.	Max.	Unit
	3.3V input		404/25	/70	mA
	5V input		250/20	/60	
Input Current (full load / no-load)	12V input		104/15	/50	
(rail load / rio load)	15V input		82/10	/35	
	24V input		52/7	/30	
Reflected Ripple Current		-	15	_	mA
Surge Voltage (1sec. max.)	3.3V input	-0.7		5	VDC

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	5V input	-0.7		9	
Curae Voltage (less may)	12V input	-0.7		18	VDC
Surge Voltage (1sec. max.)	15V input	-0.7		21	VDC
	24V input	-0.7		30	
Input Filter			Capacit	ance filter	
Hot Plug			Unav	ailable	

ltem	Operating Conditions		Min.	Тур.	Max.	Unit
Voltage Accuracy			See	output regul	ation curve(Fi	g. 1)
Linear Degulation	Input voltage change: ±1%	3.3VDC output			±1.5	
Linear Regulation		Other outputs			±1.2	
		3.3VDC output		18		%
	10%-100% load	5VDC output		12		
		6VDC output		10		
Load Regulation		9VDC output		8	-	
		12VDC output		7		
		15VDC output		6		
		24VDC output		5		
Ripple & Noise*	20MHz bandwidth			60	150	mVp-r
Temperature Coefficient	Full load		-		±0.03	%/℃
No. and a local March and and the	F03xxXT-1WR2/F24xxXT-1WR2/F0524XT-1WR2 Others				1	s
Short-circuit Protection**			Continuous, self-recovery			

Notes: \* The "parallel cable" method is used for Ripple and Noise test, please refer to DC-DC Converter Application Notes for specific information;

\*\* Supply voltage must be discontinued at the end of short circuit duration for F03xxXT-1WR2 series, F24xxXT-1WR2 series, and F0524XT-1WR2 model.

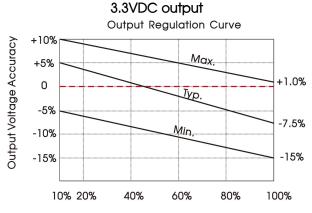
General Specifications					
Item	Operating Conditions	Min.	Тур.	Max.	Unit
Isolation Voltage	Input-output Electric strength test for 1 minute with a leakage current of 1mA max.				VDC
Insulation Resistance	Input-output resistance at 500VDC	1000			ΜΩ
Isolation Capacitance	Input-output capacitance at 100kHz/0.1V		20		рF
Operating Temperature	Derating when operating temperature up to 100 $^{\circ}\!$	-40		105	
Storage Temperature		-55		125	°C
Case Temperature Rise	Ta=25°C, nominal input, full load output		25		
Pin Soldering Resistance Temperature	Soldering spot is 1.5mm away from case for 10 seconds			300	
Storage Humidity	Non-condensing			95	%RH
Reflow Soldering Temperature time≤60s For actual		np.≤245°C, s over 217°C al applicatio C J-STD-020	on, please		
Switching Frequency	Full load, nominal input voltage	-	100		kHz
MTBF	MIL-HDFK-217F@25℃	3500			k hours

Mechanical Specifications		
Case Material	Black epoxy resin; flame-retardant heat- resistant (UL94 V-0)	
Dimensions	12.70 x 11.20 x 7.25 mm	
Weight	1.6g(Typ.)	
Cooling Method	Free air convection	

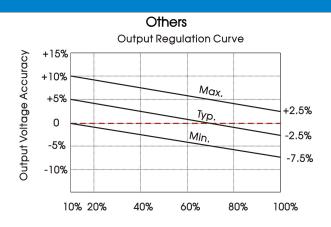


Electromagnetic Compatibility (EMC)				
Emissions	CE	CISPR32/EN55032 CLASS B (see Fig. 4 for recommended circuit)		
ETTISSIONS	RE	CISPR32/EN55032 CLASS B (see Fig. 4 for recommended circuit)		
Immunity	ESD	IEC/EN61000-4-2 Contact ±8kV perf. Criteria B		

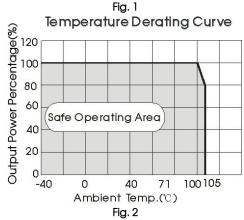
# Typical Characteristic Curves

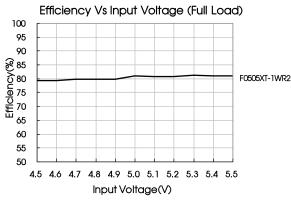


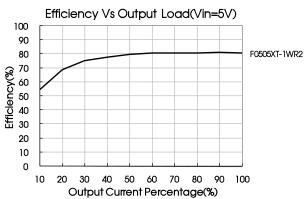




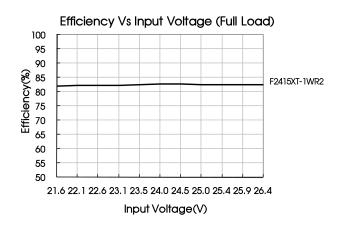
Output Current Percentage (Nominal Input Voltage)

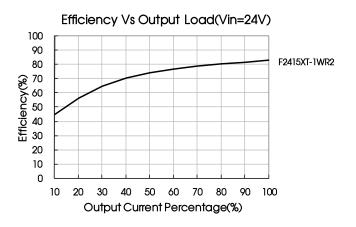










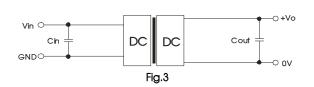


# Design Reference

## 1. Typical application circuit

Input and/or output ripple can be further reduced, by connecting a filter capacitor from the input and/or output terminals to ground as shown in Fig.3.

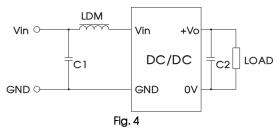
Choosing suitable filter capacitor values is very important for a smooth operation of the modules, particularly to avoid start-up problems caused by capacitor values that are too high. For recommended input and output capacitor values refer to Table 1.



#### Recommended capacitive load value table (Table 1)

Vin	Cin()	Vo	Cout
3.3VDC	4.7µF/16V	3.3VDC	10µF/16V
5VDC	4.7µF/16V	5/6VDC	10µF/16V
12VDC	2.2µF/25V	9VDC	4.7µF/16V
15VDC	2.2µF/25V	12VDC	2.2µF/25V
24VDC	1µF/50V	15VDC	1µF/25V
		24VDC	0.47µF/50V

## 2. EMC (CLASS B) compliance circuit



Input voltage		3.3/5/12/15/24VDC
Emissions	C1	4.7µF /50V
	C2	Refer to the Cout in Fig.3
	LDM	6.8µH

#### 3. Output load requirements

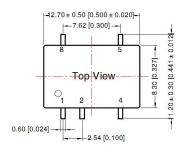
For a reliable and efficient operation of the converter, the minimum load should never be less than 10% of the rated output load. If the total required output power is below 10%, a parallel bleeding resistor is required on the output, ensuring that the sum of the power consumption is always maintained at 10% minimum.

4. For additional information, please refer to DC-DC converter application notes on <a href="https://www.mornsun-power.com">www.mornsun-power.com</a>

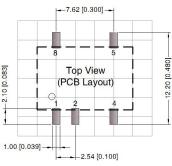


## **Dimensions and Recommended Layout**



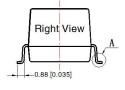






Front View

| View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | View | Vi



Note: Grid 2.54\*2.54mm

Pin-	-Out
Pin	Mark
1	GND
2	Vin
4	OV
5	+Vo
8	NC

NC: Pin to be isolated from circuitry

#### Note:

Unit: mm[inch]

Pin section tolerances:  $\pm 0.10[\pm 0.004]$ General tolerances:  $\pm 0.25[\pm 0.010]$ 

### Notes:

- For additional information on Product Packaging please refer to <u>www.mornsun-power.com</u>. Packaging bag number: 58210024, Roll Packaging bag number: 58200054;
- 2. If the product is not operated within the required load range, the product performance cannot be guaranteed to comply with all parameters in the datasheet;
- 3. The maximum capacitive load offered were tested at input voltage range and full load;
- 4. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated output load;
- 5. All index testing methods in this datasheet are based on our company corporate standards;
- 6. We can provide product customization service, please contact our technicians directly for specific information;
- Products are related to laws and regulations: see "Features" and "EMC";
- 8. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

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