



FEATURES

- Wide input voltage range: 9-36V, 14-160V
- Operating ambient temperature range: -40°C to +105°C
- With peripherals, it can be realized: input inrush current suppression, input anti-reverse connection protection, input under-voltage protection

The FS-CXZP(-N) & FS-AXZP(-N) product is a driver module, which can realize input under-voltage protection, input anti-reverse connection protection, and input inrush current suppression functions with the peripheral, which can be applied to the front end of the product, to protect back-end products and improve product reliability.

Selection Guide					
Certification		Input Voltage (VDC)			
	Part No.	Nominal (Range)	Max. [®]	Outb logic [®]	Capacitive Load (nF)Max.
_	FS-AXZP	24 (9-36) 110 (14-160)	40	Р	10
	FS-AXZP-N			N	10
	FS-CXZP		170	Р	10
	FS-CXZP-N			N	10

Notes:

① Exceeding the maximum input voltage may cause permanent damage;

2 "P" indicates that OUTB is positive logic, and "N" indicates that OUTB is negative logic. For details, see design reference.

Recommended Matching Products					
Part No.	Matching test P/N.	Recommended current limiting Wire-wound Resistor	Recommended Mosfet specification		
FS-AXZP	URF24_QB-100WR3	Four 12 Ω /3W in parallel	Specification:Vds>150V / ID>60A / VGS(th)<5V		
FS-AXZP-N	VCF24_EBO-120WR3-N	Four $12\Omega/3W$ in parallel	Specification:Vds>150V / ID>60A / VGS(th)<5V		
FS-CXZP	URF1D_HB-250WHR3	Three $51 \Omega/3W$ in parallel	Specification:Vds>650V / ID>40A / VGS(th)<5V		
FS-CXZP-N	URF1D24QB-50WR3-FD	Three $51 \Omega/3W$ in parallel	Specification:Vds>650V / ID>40A / VGS(th)<5V		

Notes:

①The selection of current limiting resistor can be based on the size of inrush current inhibition. The selection of current limiting resistor should consider whether the transient power consumption of the resistor itself meets the specifications;

©The recommended parameter is the test result of the corresponding switching power module at normal temperature. At high temperature, the temperature rise of the MOSFET should be evaluated and heat dissipation measures should be reasonably increased.

Recommended Parameter Setting							
Pin	Function	Description		Min.	Тур.	Max.	Unit
1	GNDA	The module refers to ground A					VDC
2	TIMEA	At the positive phase input end of the operational amplifier, set the module drive OUTA2 opening time T OUTA2 opening time T:the delay from start-up to OUTA1 voltage of 5V		10	25	40	ms
3 UVPA	Set the module drive OUTA2 turn-off	FS-AXZP(-N)	5.5	7	8.5	VDC	
		Voltage FS-CXZP(-N)		8.5	10	11.5	VDC

MORNSUN®

MORNSUN Guangzhou Science & Technology Co., Ltd.

2023.04.18-A/0 Page 1 of 6 MORNSUN Guangzhou Science & Technology Co., Ltd. reserves the copyright and right of final interpretation

Input anti-reverse connection and input inrush current suppression module FS-CXZP(-N) & FS-AXZP(-N) Series

MORNSUN®

0		innut anti-royana function duivar	FS-AXZP(-N)	6	8.5	9.5	VDC
9	OUIAT		FS-CXZP(-N)	6.5	8.3	9.5	VDC
		input inrush current suppression function	FSAXZP(-N)	5	7.5	9	VDC
11	OUIA2	drive	FS-CXZP(-N)	5.5	7.5	9	VDC
		The OUTB action delay of FS-A(C)XZP(-N)	FS-A(C)XZP: Delay time T1 from OUTA2 voltage rising to 5V to OUTB voltage rising to 3.5V	0.001		5	ms
		is T1	FS-A(C)XZP-N: Delay time T1 from when OUTA2 voltage rises to 5V to when OUTB voltage drops to 1.2V	0.001		5	ms
14	OUTB	Controls the Ctrl pin voltage of the power module	FS-A (C) XZP: indicates the OUTB output voltage in the delay time T	0		1.2	VDC
			FS-A (C) XZP-N: indicates the OUTB output voltage in the delay time T	3.5		12	VDC
			FS-A (C) XZP: OUTB output voltage after the delay time T1 ends	3.5		12	VDC
			FS-A (C) XZP-N: indicates the OUTB output voltage after the delay time T1 ends	0		1.2	VDC
16	GNDB	The module refers to ground B					VDC
22	NC	Cannot connect to any external circuit					/
		Surge Voltage (Jaco may)	FS-AXZP(-N)	-0.7		50	VDC
		Surge voitage (Tsec. max.)	FS-CXZP(-N)	-0.7		200	VDC
23	\ //N 1 A	Module power supply and input voltage detection	FS-AXZP(-N)	9		36	VDC
20			FS-CXZP(-N)	14		160	VDC
		Input Current	FS-CXZP(-N), 110V input voltage	3	6	12	mA
		Input Current	FS-AXZP(-N), 24V input voltage	3	6	12	mA

Notes:

1)TIMEA pin delay time can be realized by the external capacitor C1 delay time becomes longer, please see the specific design reference;

2)UVPA pin setting module drive OUTA2 off voltage can be realized through the external resistor to raise the undervoltage point, please see the design reference for details;

3)Pin 2, 3, 9, 11, 14, 23 do not short circuit to ground (pins 1, 16), pin 2, 3, 9, 11 do not connect the external level, please see the design reference for connection method;

4)The above conditions are measured under the conditions in Fig 1;

5)Use the OUTB pin to ensure that the delay from 0V to the back-end power module is greater than 3ms.

General Specifications					
ltem	Operating Conditions	Min.	Тур.	Max.	Unit
Operating Temperature		-40		+105	
Storage Temperature		-55		+125	°C
Pin Soldering resistor Temperature	Soldering spot is 1.5mm away from case for 10 seconds			+300	

MORNSUN®

MORNSUN Guangzhou Science & Technology Co., Ltd.

MORNSUN Guangzhou Science & Technology Co., Ltd. reserves the copyright and right of final interpretation

^{2023.04.18-}A/0 Page 2 of 6

Input anti-reverse connection and input inrush current suppression module FS-CXZP(-N) & FS-AXZP(-N) Series

MORNSUN[®]

Storage Humidity	Non-condensing	5		95	%RH
Shock And Vibration		IEC,	/EN61373 - Co	tegory 1, Gra	ide B
MTBF	MIL-HDBK-217F@25°C	1000			k hours
ON/OFF frequency	Minimum rec	quirement 5s			
Hot Plug			Unavo	ailable	
Note:	·				

The inrush current suppression module must be in the off state before switching on and off each time. That is, the input voltage of the module is less than the undervoltage point.

Mechanical Specific	Mechanical Specifications			
Case Material	Aluminum alloy			
Dimensions	32.00 x 20.00 x 10.80 mm			
Walaht	FS-CXZP(-N) 10.3g(Typ)			
weigili	FS-AXZP(-N) 9.5g(Typ)			
Cooling Method	Free air convection			

Electromagnetic Compatibility (EMC)					
EMI	CE/RE	Test with the correst specifications are	Test with the corresponding power module and the periphery of the supporting test module, and the specifications are the same as the test specifications of the corresponding power module.		
	ESD	IEC/EN61000-4-2	Contact ±6KV	perf. Criteria A	
EMS	RS	IEC/EN61000-4-3	10V/m	perf. Criteria A	
	EFT	IEC/EN61000-4-4	100KHz ±2KV	perf. Criteria A	
	Surge	IEC/EN61000-4-5	line to line ±2KV	perf. Criteria A	
	CS	IEC/EN61000-4-6	10 Vr.m.s	perf. Criteria A	

Note:

1)When the product is matched for EMS test, the product is tested with the corresponding power module and the periphery of the supporting test module, and the specifications are the same as those of the corresponding power module;

2)When conducting EMC test, the test specification cannot be higher than the EMC index in the table;

3)The judgment result of EMS shall be subject to the output of power module.

Design Reference

1. Recommended circuit for single test:

The recommended connection diagram of FS-AXZP(-N) and FS-CXZP(-N) is as follows:



Fig. 1

Table 1: Fig. 1 Recommended device parameters table

Device	Parameter	Parameter description
	FS-CXZP(-N) Selec 20D201K	/
NOV	FS-AXZP(-N) Selec 20D470K	
R1	3.47KΩ 1/2W	/

MORNSUN®

MORNSUN Guangzhou Science & Technology Co., Ltd.



2. Recommended circuit for application:

The recommended connection diagram of FS-AXZP(-N) and FS-CXZP(-N) is as follows:



Fig. 2

Table 2: Fig 2 Recommended device parameters

Dovico	Paramotor	Parameter description
Device		
MOV	FS-CXZP(-N) Select 2D201K	
1010 0	FS-AXZP(-N) Select 20D470K	
	See the recommended collocation	1
	table above	
	1	See Design Reference 3 Undervoltage
RI		Protection Design for instructions
D0	See the recommended collocation	1
RZ	table above	
50	FS-CXZP(-N) Select 27 Ω 1/4W	1
R3	FS-AXZP(-N) Select 8.2 Ω 1/4W	
R4	FS-A(C)XZP(-N) Select 500 Ω 5W	1
	1	See Design Reference 4 for instructions on how
		to extend inrush current suppression time
L1	100uH	1

Product working principle:

Taking FS-A(C)XZP as an example, according to the connection method in Fig 2, after the input power supply turns on the switch, the voltage of the FS-A(C)XZP module rises, and OUTA1 outputs a high level to drive the anti-reverse connection Mosfet to make it conduct, the input voltage charges the large capacitor in the switching power supply module and the capacitor in the EMC module through the resistor R2. During this process, the OUTB pin outputs a low level and pulls down the CTRL pin of the switching power supply, so that even if the switching power supply reaches the starting voltage, it will not start to work, after the charging delay of the capacitor T junction, OUTA2 outputs high level, the Mosfet at both ends of the current limiting resistor are turned on, and after a delay T1, the OUTB pin outputs high level, and the switching power supply starts to work. The working principle of FS-A(C)XZP-N is basically the same, only the logic of OUTB pin is opposite.

Notes:

1)The definition of delay I and I1 can be seen in the recommended parameter setting table;

2)OUTA1, OUTA2, OUTB output level range can be seen in the recommended parameter setting table;

3)When the trace at A in the recommended circuit is long, it is recommended to connect an inductor with an induceance of 2mH inseries in the two traces respectively, and to connect a decoupling capacitor in parallel between CTRL and GND of the switching power supply.

3. Undervoltage protection design:

As shown in Fig. 1, UVPA pin drives OUTA2 turn-off voltage V1 through external resistor R1 to GNDA setting module, and this voltage is the undervoltage point of VINA pin. When the voltage is lower than turn-off voltage V1, OUTA2 pin voltage drops below 0.7V.

$$V1 = \frac{12410R1 + 1130791}{1220R1 + 6222}$$

Formula 1



MORNSUN Guangzhou Science & Technology Co., Ltd.

MORNSUN Guangzhou Science & Technology Co., Ltd. reserves the copyright and right of final interpretation



$$V1 = \frac{1096R1 + 120045}{157R1 + 3140}$$

Formula 2

Notes:

1)Module drive OUTA2 turn-off voltage V1, unit is V;

2)R1 is the external resistor, expressed in k Ω ;

3)Formula 1 applies to model FS-CXZP (-N), and Formula 2 applies to model FS-AXZP (-N); 4)When the UVPA pin is suspended, the voltage of V1 is shown in the undervoltage point of VINA in the parameter specification;

5)You are advised to set this voltage to the lower limit of the actual working voltage of the power module.

4. Extend inrush current suppression time:

In the connection method shown in Fig 1, the TIMEA pin sets the delay time T of 5V output voltage of the module from startup to OUTA2 through the external capacitor C1 to GNDA.

$$T = 50(0.5 + C1)$$

Notes:

1)Module driver OUTA2 starts time T (unit: ms);

2)C1 is the external capacitor, expressed in uF;

3) When the TIMEA pin is suspended, see the opening time of driver OUTA2 in parameter specifications for T time;

4)It is recommended to set this time to be greater than the input inrush current time;

5)Due to the capacitance deviation, the actual test time at normal temperature will be deviated by $\pm 10\%$, and the capacitance value of the capacitor will change at high and low temperatures. Affected by the temperature characteristics of the selected capacitor, it is recommended to use a capacitor with good temperature characteristics.

5. Recommended solution of thermal test

In the process of application, product thermal design can be evaluated by combining product temperature derating curve, or determine the stable working interval of the product by the temperature at point B in the following test figure. When the temperature at point B is lower than 115° , it is the stable working interval of the product.



MORNSUN®

MORNSUN Guangzhou Science & Technology Co., Ltd.

MORNSUN[®]

Dimensions and Recommended Layout



Note: Unit: mm[inch] PIN1/2/3/9/11/14/16/22/23: ϕ 0.5mm Pin diameter tolerances: \pm 0.10[\pm 0.004] General tolerances: \pm 0.50[\pm 0.020]



Note: Grid 2.54*2.54mm

Pin-Out			
Pin	Mark		
1	GNDA		
2	TIMEA		
3	UVPA		
9	OUTA1		
11	OUTA2		
14	OUTB		
16	GNDB		
22	NC		
23	VINA		

NC: Pin to be isolated from circuit

Notes:

- 1. For additional information on Product Packaging please refer to www.mornsun-power.com. Packaging bag number: 58210008;
- 2. The maximum capacitive load offered were tested at input voltage range and full load;
- 3. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated output load;
- 4. All index testing methods in this datasheet are based on company corporate standards; We can provide product customization service, please contact our technicians directly for specific information;
- 5. Products are related to laws and regulations: see "Features" and "EMC";
- 6. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

Mornsun Guangzhou Science & Technology Co., Ltd.

 Address: No. 5, Kehui St. 1, Kehui Development Center, Science Ave., Guangzhou Science City, Huangpu District, Guangzhou, P. R. China

 Tel: 86-20-38601850
 Fax: 86-20-38601272

 E-mail: info@mornsun.cn
 www.mornsun-power.com

MORNSUN®

MORNSUN Guangzhou Science & Technology Co., Ltd.

2023.04.18-A/0 Page 6 of 6

MORNSUN Guangzhou Science & Technology Co., Ltd. reserves the copyright and right of final interpretation