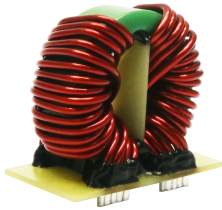


Filter Inductance



RoHS



FEATURES

- High reliability
- Effective anti-interference
- Good temperature characteristic (-40℃~+115℃)

The filter Inductance is suitable for applications that are sensitive to noise, such as analog circuits , and the EMC level can be significantly improved by adding a product to the input/output of the power module.

Selection Guide

Type	Model	Inductance (uH)	Current (A)(max)	DCR(mΩ) (max)	Weight (g) (typ)	Size(mm) (L x W x H)
Nickel-zinc inductance	FL2D-A0-900	90	10	10	26	31.00 x 16.00 x 28.50 See Figure1
	FL2D-F0-100	10	60	1	119	51.00 x 28.50 x 46.00 See Figure2
	FL2D-F5-040	4	65	1	85	49.50 x 28.50 x 44.00 See Figure3
	FL2D-Z5-140	14	0.5	100	0.6	10.00 x 8.00 x 7.00 See Figure4

Note: The inductance values above are tested at room temperature 25℃,FL2D-A0-900 in the table is a typical value, and other values are the minimum

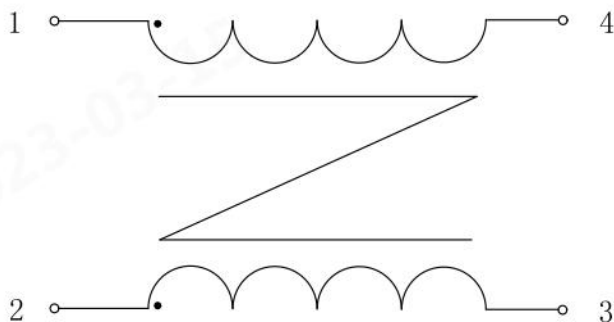
General Specifications

Item	Test Conditions		Min.	Typ.	Max.	Unit
Operating Temperature *			-40	--	+115	℃
Storage Temperature			-40	--	+105	
Welding Temperature	Wave soldering welding,10 seconds MAX		255	260	265	
Storage Humidity	Non-Condensing		--	--	95	%
Inductance	fo=10KHz, Uo=0.1V,T=25℃	FL2D-A0-900	58.5	90	121.5	uH
		FL2D-F0-100**	10	--	--	
		FL2D-F5-040**	2	4	6	
		FL2D-Z5-140	14	--	--	
Isolation Voltage (COIL-COIL)	Electric strength test for 1 minute with a leakage current of 5mA max		--	--	1000	VAC
Vibration			10~55Hz, 10g, X,Y,Z direction ,2mm			
Warranty	Ambient temperature: <40℃		3 years			

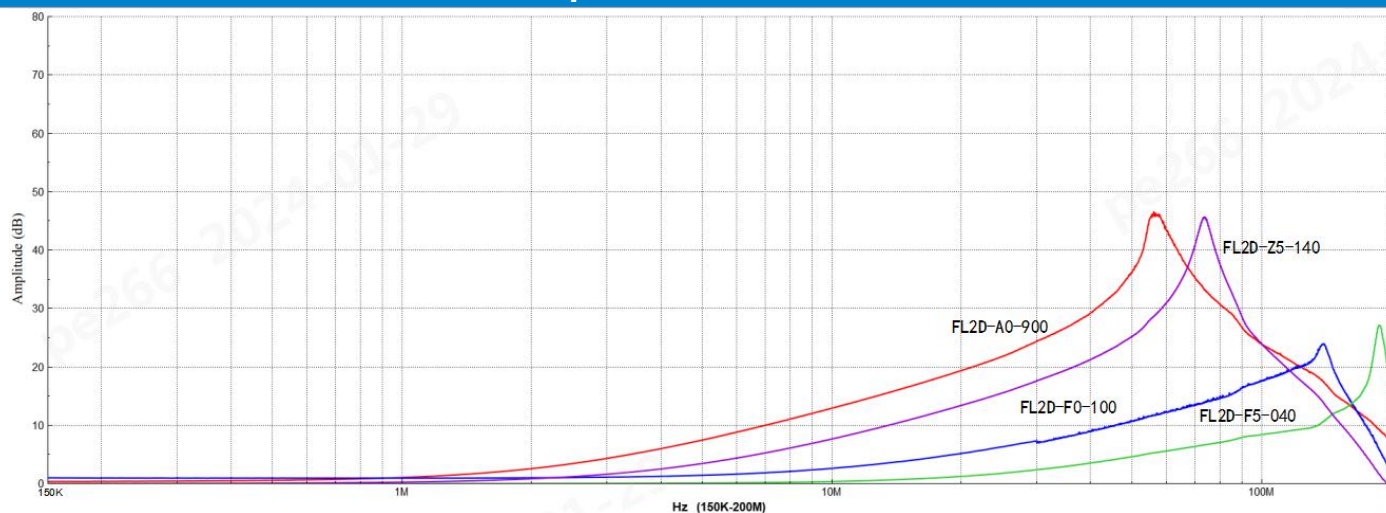
Note: *1.Operating Temperature range includes inductor self heating.

**2.The dispensing advice added fixed when using this product.

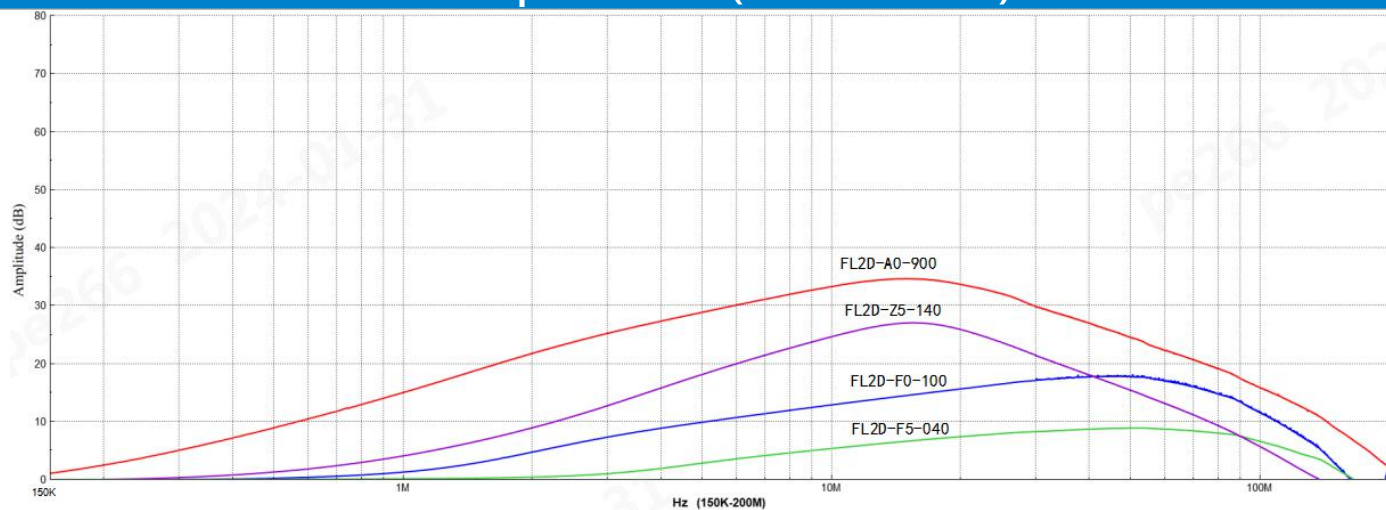
Schematic Diagram Of Pin Mark



Differential Mode Insertion Loss Specifications (reference value)



Common Mode Insertion Loss Specifications (reference value)



FL2D-A0-900 Schematic Diagram Of Pin Mark

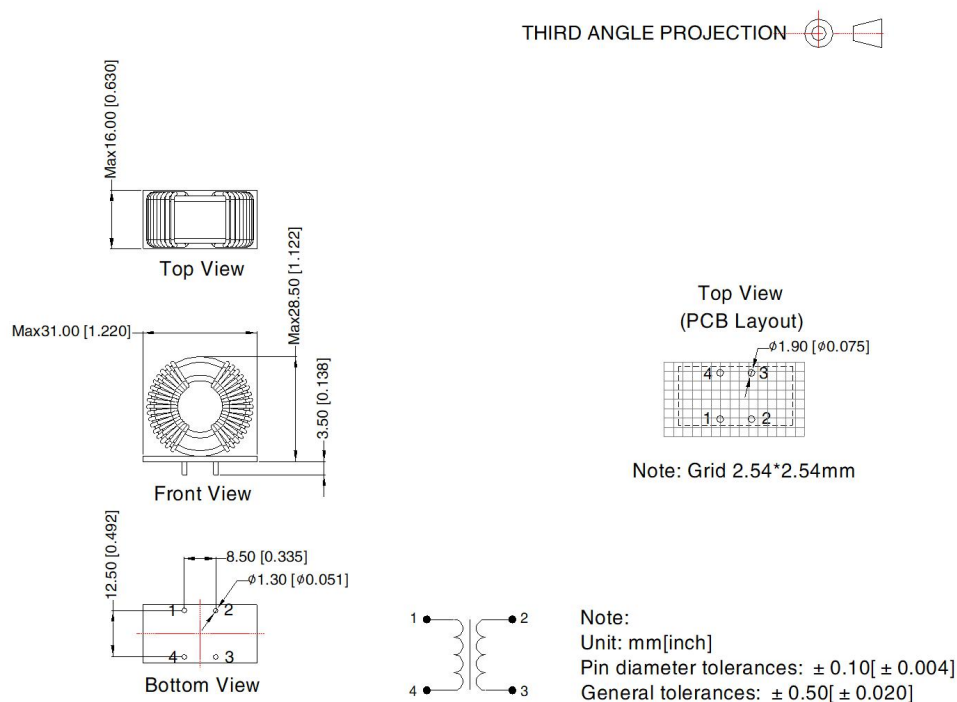


Fig. 1

FL2D-F0-100 Schematic Diagram Of Pin Mark

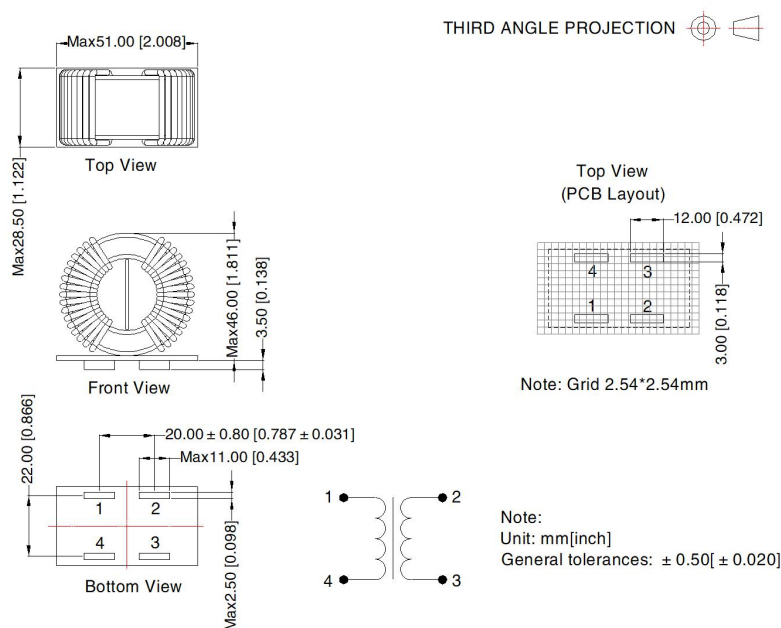


Fig. 2

FL2D-F5-040 Schematic Diagram Of Pin Mark

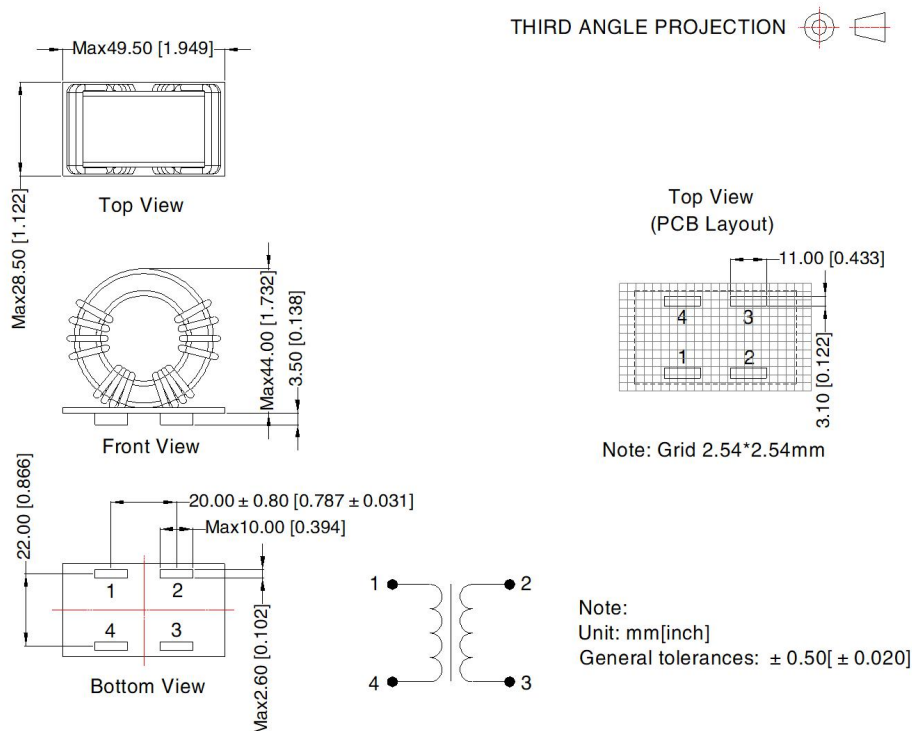


Fig. 3

FL2D-Z5-140 Schematic Diagram Of Pin Mark

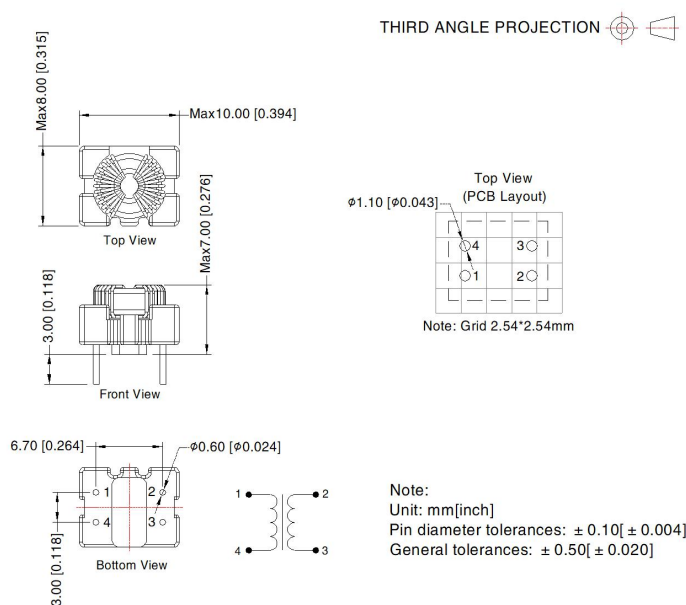


Fig. 4

Note:

1. For additional information on Product Packaging please refer to www.mornsun-power.com. Packaging bag number: 58210394(FL2D-A0-900)、58210403 (FL2D-F0-100、FL2D-F5-040) 、58210400 (FL2D-Z5-140) ;
1. Unless otherwise specified, data in this data sheet should be tested under the conditions of Ta=25°C, humidity<75%RH;
2. All index testing methods in this datasheet are based on company corporate standards;
3. The performance indexes of the product models listed in this manual are as above, but some indexes of non-standard model products will exceed the above-mentioned requirements, and please directly contact our technicians for specific information;
4. We can provide product customization service, please contact our technicians directly for specific information;
5. Products are related to laws and regulations: see "Features" ;
6. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

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