EMC Filter

FL2D-40-123 & FL2D- 60-702

MORNSUN

Filter Inductance

RoHS

FEATURES

- Compact size

- High reliability

- Highly cost effective

- Effective anti-interference

The filter Inductance is suitable for applications that are sensitive to noise, such as analog circuits , and the EMC level can be significantly improved by adding a product to the input/output of the power module.

Selection Guide

Type	Model	*Inductance (uH)(min)	Current (A)(max)	DCR(mΩ) (max)	Weight (g) (typ)	Size(mm) (L x W x H)	
Common Mode Choke	FL2D-40-123	12	4	150	20	26.50 x 25.50 x 16.00	See Figure1
	FL2D-60-702	7	6	75	21	23.00 x14.50 x 31.50	See Figure2

General Specifications

| Item	Test Conditions		Min.	Typ.	Max.	Unit
Operating Temperature *			-40	--	+120	°C
Storage Temperature			-40	--	+105	
Storage Humidity	Non-Condensing		--	--	95	%
Inductance	fo=1KHz, Uo=0.25V,T=25°C	FL2D-40-123	12	--	--	mH
		FL2D-60-702	7	--	--	
Isolation Voltage（COIL-COIL）	Electric strength test for 1 minute with a leakage current of 5mA max		--	--	1000	VAC
Vibration		10~55Hz, 10g, X,Y,Z direction ,2mm				
Warranty	Ambient temperature: <40°C	3 years				
Note: *1.Operating Temperature range includes inductor self heating.						

Schematic Diagram Of Pin Mark
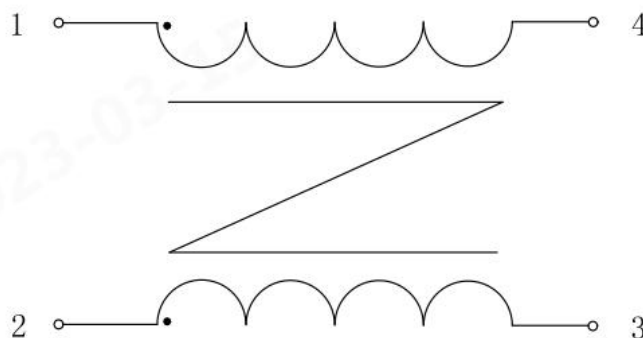
Differential Mode Insertion Loss Specifications (reference value)
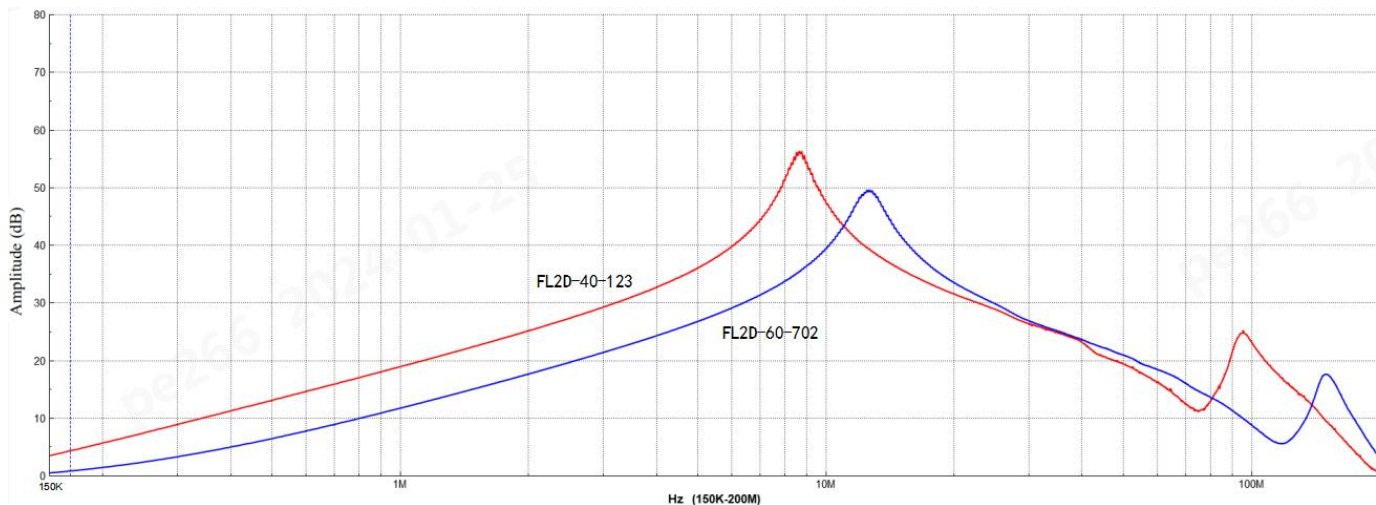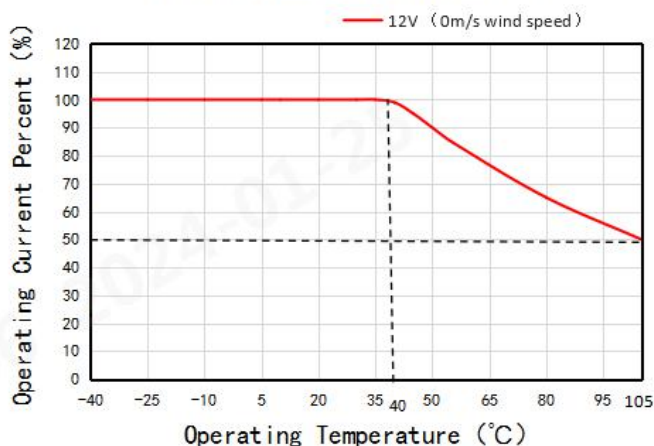

Common Mode Insertion Loss Specifications (reference value)
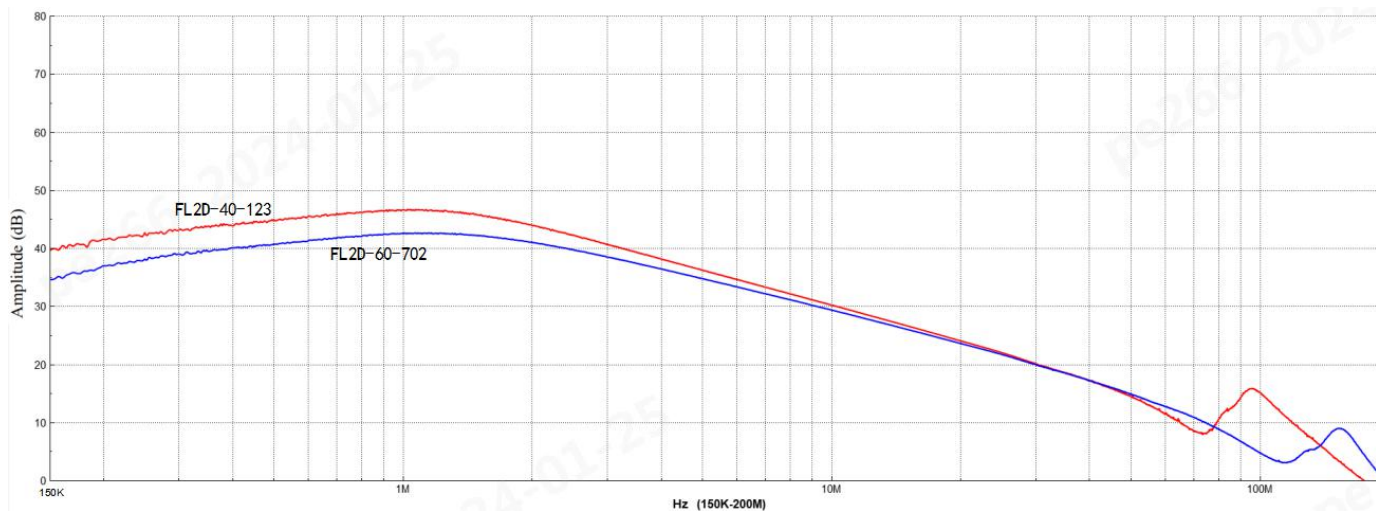
Roduct Typical Curve

Temperature Derating Curve

Note: This product is suitable for applications using natural air cooling; for applications in closed environment please consult factory or one of our FAE.
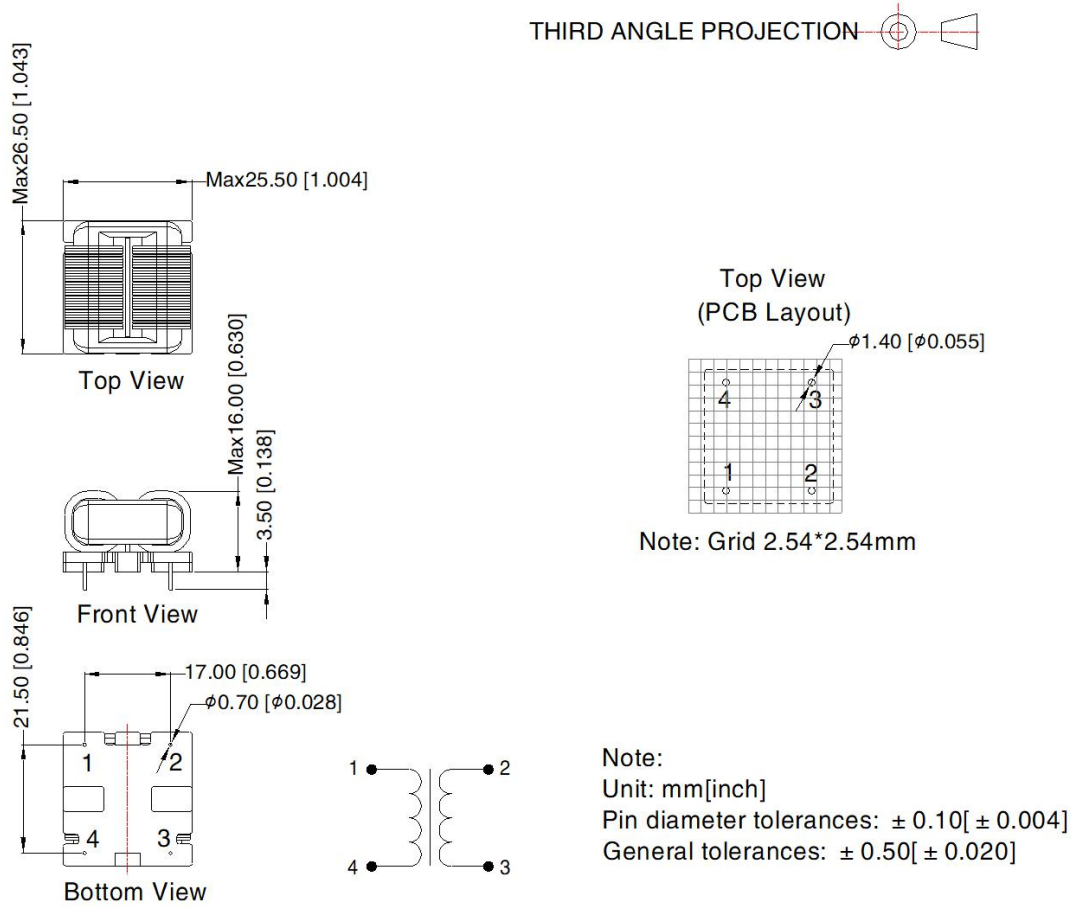
FL2D-40-123 Schematic Diagram Of Pin Mark

THIRD ANGLE PROJECTION

Top View

Front View

Bottom View

Top View
(PCB Layout)

Note: Grid 2.54*2.54mm

Note:
Unit: mm[inch]
Pin diameter tolerances: ± 0.10[± 0.004]
General tolerances: ± 0.50[± 0.020]

Fig. 1

FL2D- 60-702   Schematic Diagram Of Pin Mark

THIRD ANGLE PROJECTION ⊕▷

Top View

Max14.50 [0.571]

Max31.50 [1.240]

Max23.00 [0.906]

3.50 [0.138]

Front View

10.00 [0.394]

13.00 [0.512]

φ0.80 [φ0.031]

Bottom View

Top View
(PCB Layout)

φ1.40 [φ0.055]

Note: Grid 2.54*2.54mm

Note:
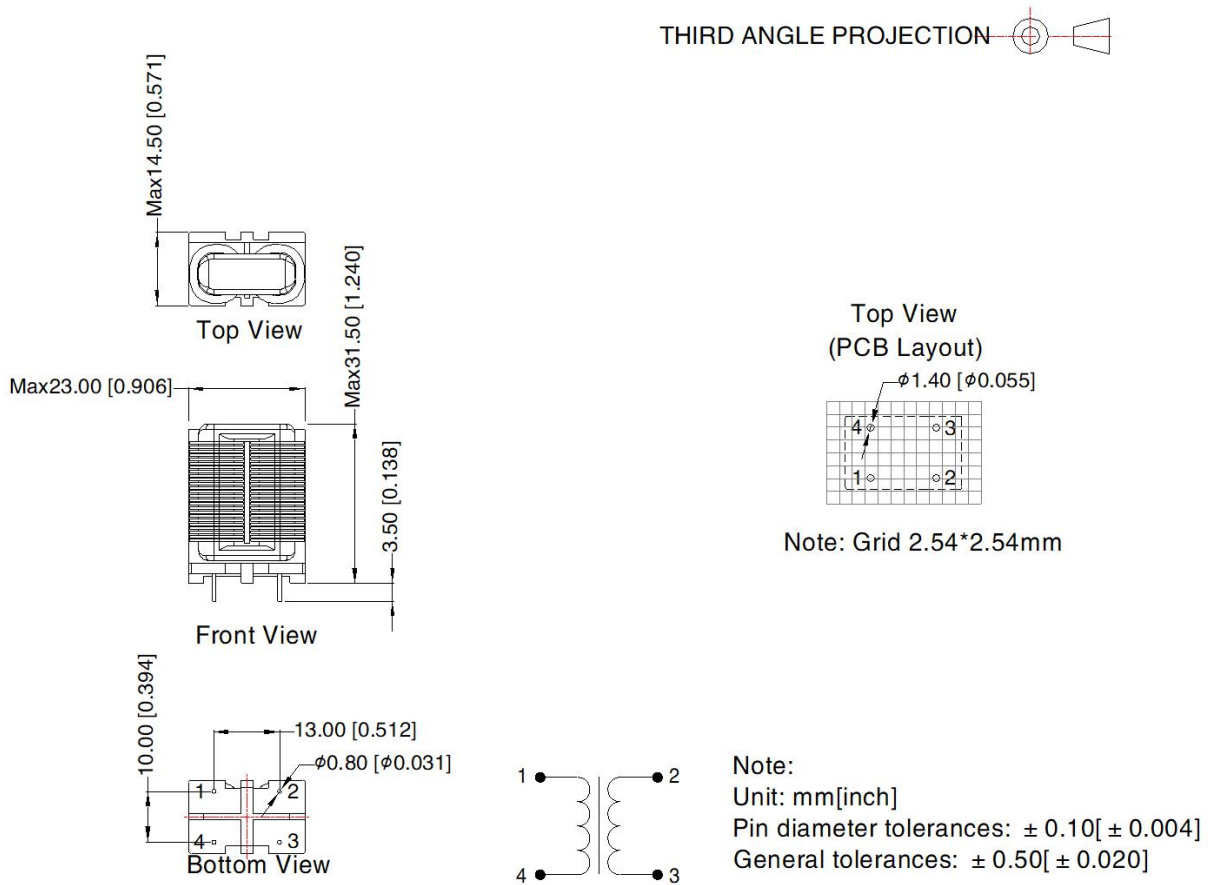Unit: mm[inch]
Pin diameter tolerances: ± 0.10[± 0.004]
General tolerances: ± 0.50[± 0.020]

Fig. 2

Note:

1. For additional information on Product Packaging please refer to www.mornsun-power.com. Packaging bag number: 58210396(FL2D-60-702);58210180(FL2D-40-123);

2. Unless otherwise specified, data in this data sheet should be tested under the conditions of Ta=25℃, humidity<75%RH;

3. All index testing methods in this datasheet are based on company corporate standards;

4. The performance indexes of the product models listed in this manual are as above, but some indexes of non-standard model products will exceed the above-mentioned requirements, and please directly contact our technicians for specific information;

5. We can provide product customization service, please contact our technicians directly for specific information;

6. Products are related to laws and regulations: see "Features" ;

7. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

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