

1W isolated DC-DC converter
Fixed input voltage, unregulated single output







FEATURES

- Continuous short-circuit protection
- No-load input current as low as 12mA
- Operating ambient temperature range: -40 $^{\circ}{\rm C}$ to +105 $^{\circ}{\rm C}$
- High efficiency up to 84%
- Compact SMD package
- I/O isolation test voltage 3k VDC
- Industry standard pin-out

F03_XT-1WR3 series are designed for use in distributed power supply systems and especially suitable in applications such as pure digital circuits, low frequency analog circuits, relay-driven circuits and data switching circuits.

Selection 6	Suide					
		Input Voltage (VDC)	С	Output	Full Load	Capacitive
Certification	Part No.	Nominal (Range)	Voltage (VDC)	Current(mA) Max./Min.	Efficiency (%) Min./Typ.	Load(µF) Max.
	F0303XT-1WR3		3.3	303/30	73/77	2400
	F0305XT-1WR3		5	200/20	78/82	2400
	F0309XT-1WR3	3.3	9	111/11	80/84	1000
_	F0312XT-1WR3	(2.97-3.63)	12	83/8	80/84	560
	F0315XT-1WR3		15	67/7	80/84	560
	F0324XT-1WR3		24	42/4	80/84	220

Input Specifications						
Item	Operating Cor	Operating Conditions		Тур.	Max.	Unit
		3.3VDC output	-	394/12	416/	mA
Input Current	3.3VDC input	5VDC output		370/12	389/	
(full load / no-load)	olov Bo il ipai	9VDC/12VDC/15VDC/24VDC output	_	361/12	379/	
Reflected Ripple Current*			-	30	-	
Surge Voltage (1sec. max.)			-0.7	-	5	VDC
Input Filter Capacitance filter						
Hot Plug Unavailable					railable	
Note: *Reflected ripple current test	ing method please r	efer to DC-DC Converter Application Not	e for specific op	eration.		

Item	Operating Conditi	ons	Min.	Тур.	Max.	Unit
Voltage Accuracy			See output regulation curve (Fig. 1)			
	Input voltage	3.3VDC output			±1.5	
Linear Regulation	change: ±1%	5VDC/9VDC/12VDC/15VDC/ 24VDC output	_	_	±1.2	
	10%-100% load	3.3VDC output		15	20	%
Land Danidation		5VDC output	-	10	15	
Load Regulation		9VDC/12VDC/15VDC output		8	15	
		24VDC output		6	15	
Ripple & Noise*	20MHz bandwidth			50	100	mVp-p
Temperature Coefficient	Full load		±0.02		%/℃	
Short-circuit Protection				Continuous,	self-recovery	'

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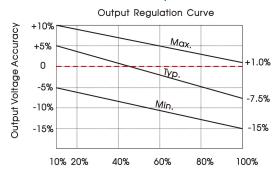
Item	Operating Conditions	Min.	Тур.	Max.	Unit
Isolation	Input-output electric strength test for 1 minute with a leakage current of 1mA max.	3000			VDC
Insulation Resistance	Input-output resistance at 500VDC	1000			ΜΩ
Isolation Capacitance	Input-output capacitance at 100kHz/0.1V		20		рF
Operating Temperature Derating when operating temperature \geqslant 85 $^{\circ}$ C, (see Fig. 2)		-40		105	
Storage Temperature		-55		125	$^{\circ}$
Case Temperature Rise	Ta=25°C		25		
Storage Humidity	Non-condensing	5		95	%RH
Reflow Soldering Temperature*		Peak te		C, maximum over 217°C	duration
Vibration		10-150)Hz, 5G, 0.75n	nm. along X, \	Y and Z
Switching Frequency	Full load, nominal input voltage	-	220		kHz
MTBF	MIL-HDBK-217F@25°C	3500			k hours
Moisture Sensitivity Level (MSL)	Moisture Sensitivity Level (MSL) IPC/JEDEC J-STD-020D.1 Level 1				

Mechanical Specifications			
Case Material	Black plastic; flame-retardant and heat-resistant (UL94 V-0)		
Dimensions	13.20 x 11.40 x 7.25 mm		
Weight	1.4g(Typ.)		
Cooling Method	Free air convection		

Electromag	Electromagnetic Compatibility (EMC)					
Emissions	CE	CISPR32/EN55032	CLASS B (see Fig. 4 for recommended circuit)			
ETHISSIONS	RE	CISPR32/EN55032	CLASS B (see Fig. 4 for recommended circuit)			
Immunity	ESD	IEC/EN61000-4-2	Air ±8kV, Contact ±6kV	perf. Criteria B		

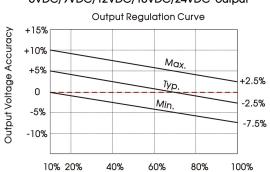
Typical Characteristic Curves





Output Current Percentage (Nominal Input Voltage)

5VDC/9VDC/12VDC/15VDC/24VDC output



Output Current Percentage (Nominal Input Voltage)

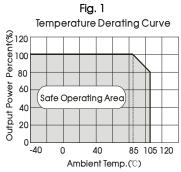
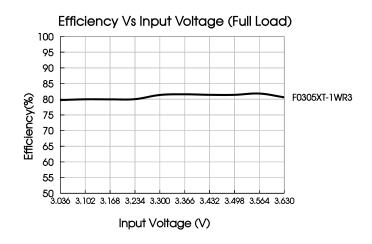


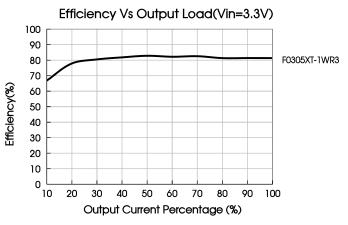
Fig. 2

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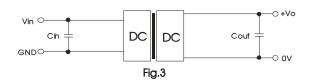


Design Reference

1. Typical application

Input and/or output ripple can be further reduced, by connecting a filter capacitor from the input and/or output terminals to ground as shown in Fig.3.

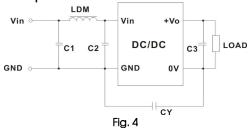
Choosing suitable filter capacitor values is very important for a smooth operation of the modules, particularly to avoid start-up problems caused by capacitor values that are too high. For recommended input and output capacitor values refer to Table 1.



IGDIO II NOCCOI	minoriaca mpai	and calpai cap	aciici valaco
Vin	Cin	Vo	Cout
3 3//DC	4 7uE/16\/	3 3//DC	10uE/16\/

	Vin Cin		VO	Coul
3.3VDC 4.7µF/16V		3.3VDC	10µF/16V	
			5VDC	10µF/16V
			9VDC	4.7µF/16V
			12VDC	2.2µF/25V
			15VDC	1µF/25V
			24VDC	0.47µF/50V

2. EMC compliance circuit



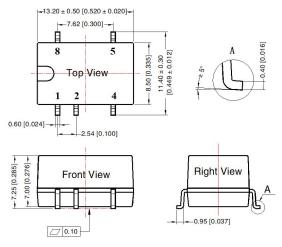
Emissions	C1, C2	4.7µF /16V
	C3	Refer to the Cout in Fig. 3
	CY	270pF/4kV
	LDM	6.8µH

3. For additional information, please refer to DC-DC converter application notes on www.mornsun-power.com

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Dimensions and Recommended Layout





Note:

Unit: mm[inch]

Pin section tolerances: $\pm 0.10[\pm 0.004]$ General tolerances: $\pm 0.25[\pm 0.010]$

Pin

5

Type

SMD

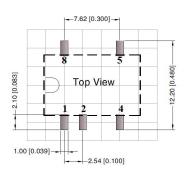
SPQ

500

Diameter

(mm)

330.0

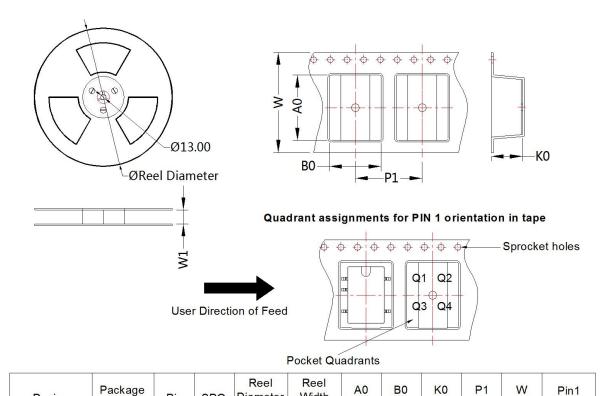


Note: Grid 2.54*2.54mm

Pin-Out				
Pin	Mark			
1	GND			
2	Vin			
4	OV			
5	+Vo			
8	NC			

NC: Pin to be isolated from circuitry

Tape and Reel Info



Width

W1 (mm)

24.5

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Device

F_XT-1WR3

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(mm)

16.0

24.0

(mm) Quadrant

Q1

(mm)

11.7

(mm)

7.5

(mm)

13.4



Notes:

- For additional information on Product Packaging please refer to <u>www.mornsun-power.com</u>. Tube Packaging bag number: 58210024, Roll Packaging bag number: 58200054;
- 2. If the product is not operated within the required load range, the product performance cannot be guaranteed to comply with all parameters in the datasheet;
- 3. The maximum capacitive load offered were tested at input voltage range and full load;
- Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated output load;
- 5. All index testing methods in this datasheet are based on our company corporate standards;
- 6. We can provide product customization service, please contact our technicians directly for specific information;
- Products are related to laws and regulations: see "Features" and "EMC";
- 8. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

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